

## Single N-Channel MOSFET

### ■ DESCRIPTION

SMC4730PA is the N-Channel enhancement mode power field effect transistors, provide superior fast switching performance and withstand high energy pulse in the avalanche and commutation mode.

### ■ PART NUMBER INFORMATION

**SMC 4730 PA - TR G**

a	b	c	d	e
---	---	---	---	---

a : Company name.

b : Product Serial number.

c : Package code PA:DFN5X6A-8

d : Handling code TR:Tape&Reel

e : Green produce code G:RoHS Compliant

### ■ FEATURES

**V<sub>DS</sub>=30V, I<sub>D</sub>=100A**

R<sub>DS(ON)</sub>=1.8mΩ(Typ.)@V<sub>GS</sub>= 10V

R<sub>DS(ON)</sub>=2.3mΩ(Typ.)@V<sub>GS</sub>= 4.5V

◆ Low Gate Charge

◆ 100% UIS and R<sub>G</sub> tested

◆ High power and current handling capability

### ■ APPLICATIONS

◆ POL Applications

◆ DC/DC Converters



### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C Unless otherwise noted)

Symbol	Parameter	Rating	Units
V <sub>DSS</sub>	Drain-Source Voltage	30	V
V <sub>GSS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Continuous Drain Current <sup>A</sup>	100	A
	T <sub>C</sub> =25°C	100	A
	T <sub>C</sub> =100°C	89	A
I <sub>DM</sub>	Pulsed Drain Current <sup>B</sup>	280	A
I <sub>D</sub>	Continuous Drain Current	27	A
	T <sub>A</sub> =25°C	21	A
P <sub>D</sub>	Power Dissipation <sup>A</sup>	2.5	W
	T <sub>A</sub> =70°C	1.6	W
I <sub>AS</sub>	Single Pulse Avalanche Current <sup>B</sup>	60	A
E <sub>AS</sub>	Single Pulse Avalanche energy L=0.1mH <sup>BE</sup>	180	mJ
P <sub>D</sub>	Power Dissipation <sup>C</sup>	69	W
	T <sub>C</sub> =25°C	28	W
T <sub>J</sub>	Operation Junction Temperature	-55/150	°C
T <sub>STG</sub>	Storage Temperature Range	-55/150	°C

### ■ THERMAL RESISTANCE

Symbol	Parameter	Typ	Max	Units
R <sub>θJA</sub>	Thermal Resistance Junction to Ambient <sup>A</sup>	t≤10s	20	°C/W
	Thermal Resistance Junction to Ambient <sup>AC</sup>	Steady-State	55	
R <sub>θJC</sub>	Thermal Resistance Junction to Case		1.8	

**ELECTRICAL CHARACTERISTICS( $T_A=25^\circ\text{C}$  Unless otherwise noted)**

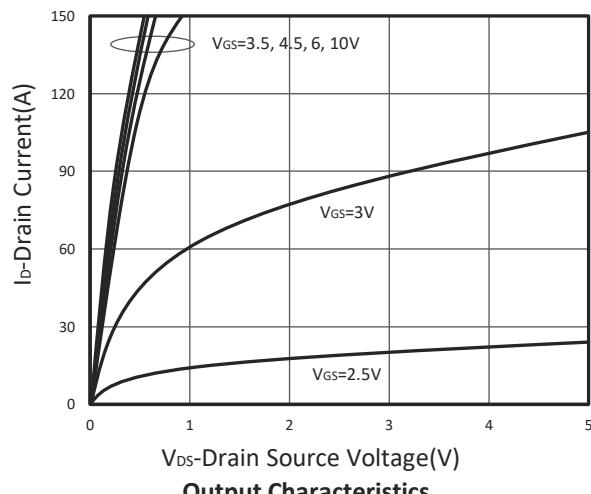
Symbol	Parameter	Condition	Min	Typ	Max	Unit	
<b>Static Parameters</b>							
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	30			V	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0	1.7	2.5	V	
$I_{GSS}$	Gate Leakage Current	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			$\pm 100$	nA	
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=30\text{V}, V_{GS}=0\text{V}, T_J=25^\circ\text{C}$			1	$\mu\text{A}$	
		$V_{DS}=24\text{V}, V_{GS}=0\text{V}, T_J=75^\circ\text{C}$			10		
$R_{DS(\text{ON})}$	Drain-source On-Resistance <sup>D</sup>	$V_{GS}=10\text{V}, I_D=25\text{A}$		1.8	2.2	$\text{m}\Omega$	
		$V_{GS}=4.5\text{V}, I_D=20\text{A}$		2.3	3		
$G_f$	Forward Transconductance	$V_{DS}=5\text{V}, I_D=20\text{A}$		85		S	
<b>Diode Characteristics</b>							
$V_{SD}$	Diode Forward Voltage <sup>D</sup>	$I_S=1\text{A}, V_{GS}=0\text{V}$			1	V	
$I_S$	Diode Continuous Forward Current				100	A	
$T_{rr}$	Reverse Recovery Time	$I_S=20\text{A}, dI/dt=100\text{A}/\mu\text{s}$		25		ns	
$Q_{rr}$	Reverse Recovery Charge			52		nC	
<b>Dynamic and Switching Parameters<sup>F</sup></b>							
$Q_g$	Total Gate Charge	$V_{DS}=15\text{V}, V_{GS}=10\text{V}, I_D=20\text{A}$		47.1	63.6	nC	
$Q_g$	Total Gate Charge (4.5V)			22	30.8		
$Q_{gs}$	Gate-Source Charge			11	15.4		
$Q_{gd}$	Gate-Drain Charge			12	17		
$C_{iss}$	Input Capacitance	$V_{DS}=15\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$		4250		pF	
$C_{oss}$	Output Capacitance			680			
$C_{rss}$	Reverse Transfer Capacitance			330			
$R_g$	Gate Resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, F=1\text{MHz}$		1.6		$\Omega$	
$t_{d(on)}$	Turn-On Time	$V_{DD}=15\text{V}, V_{GS}=10\text{V}, R_G=3.3\Omega$		15		ns	
$t_r$				9			
$t_{d(off)}$	Turn-Off Time			42			
$t_f$				13			

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

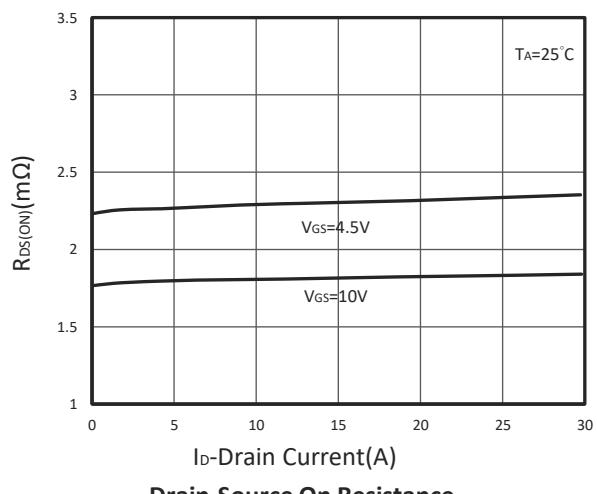
- A. Surface mounted on FR4 board using 1 in<sup>2</sup> pad size.
- B. Pulsed width limited by maximum junction temperature,  $T_J(\text{MAX})=150^\circ\text{C}$  (initial temperature  $T_J=25^\circ\text{C}$ ).
- C. Using  $\leq 10\text{s}$  junction-to-ambient thermal resistance is base on  $T_J(\text{MAX})=150^\circ\text{C}$ .
- D. Pulse test width  $\leq 300\mu\text{s}$  and duty cycle  $\leq 2\%$ .
- E. The EAS data shows maximum, The test condition is  $V_{DD}=25\text{V}, V_{GS}=10\text{V}, L=0.1\text{mH}, I_{AS}=60\text{A}$ .
- F. Guaranteed by design, not subject to production testing.
- \* . The maximum rating current limited by package.

The products and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date. We assume no responsibility for any infringement of patents, patent rights, or other rights arising from the use of any information and circuitry in this datasheet.

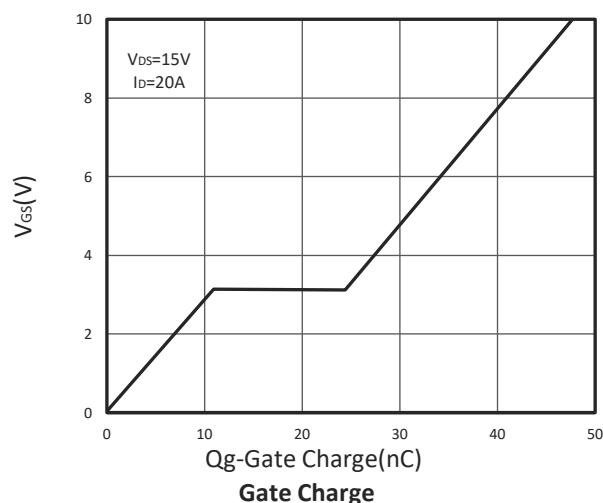
## TYPICAL CHARACTERISTICS



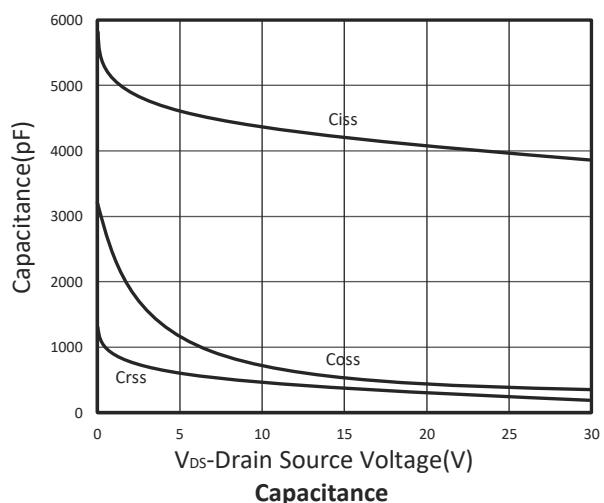
Output Characteristics



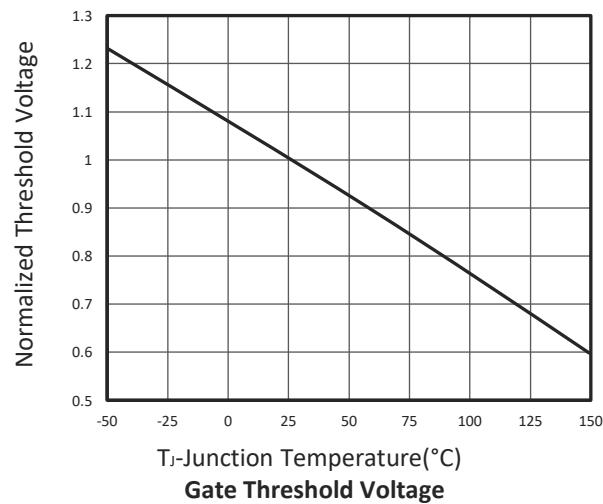
Drain-Source On Resistance



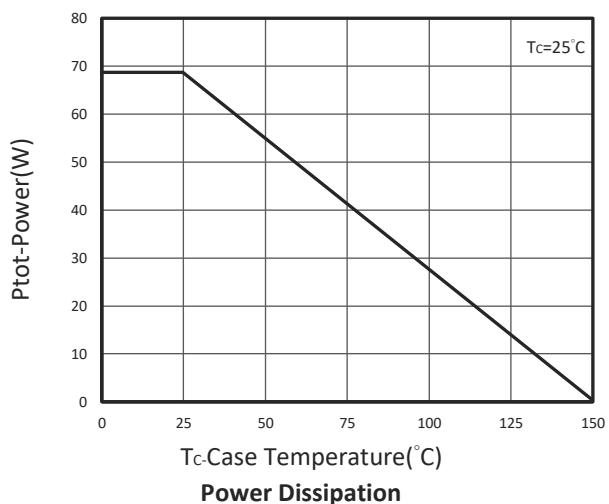
Gate Charge



Capacitance

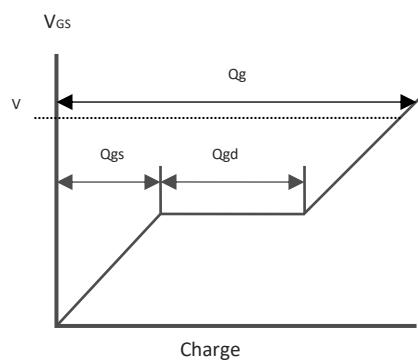
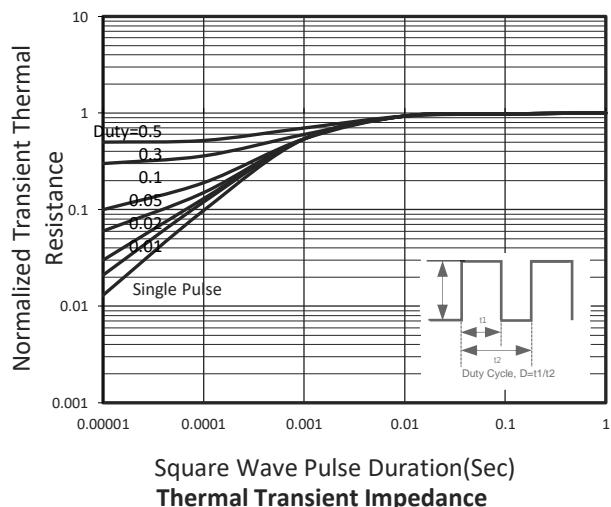
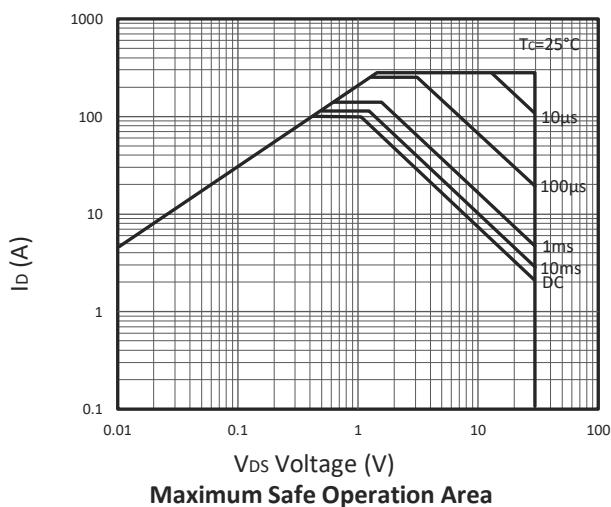
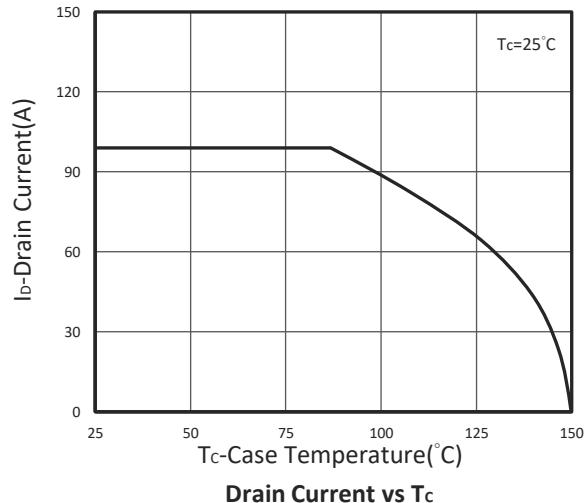
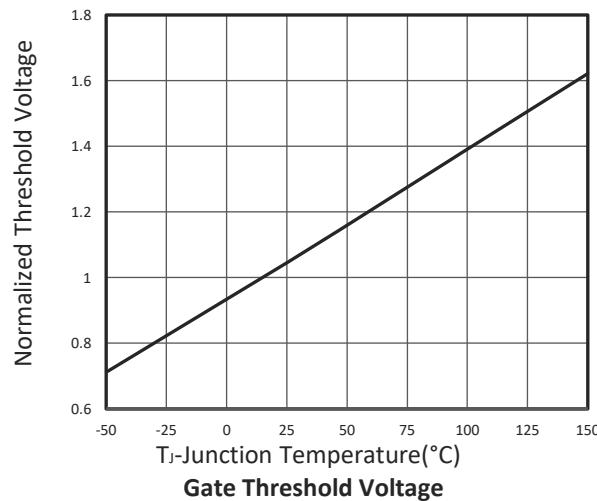


Gate Threshold Voltage

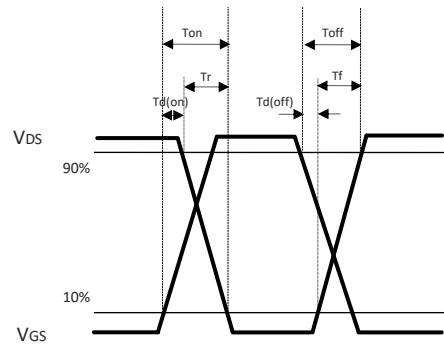


Power Dissipation

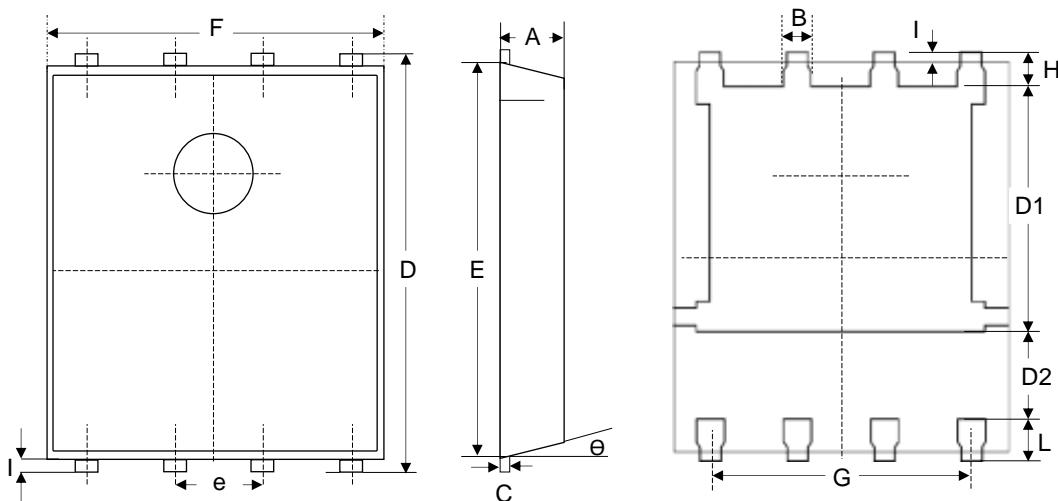
## TYPICAL CHARACTERISTICS



Gate Chrg Waveform



Switching Time Waveform

**■ DFN5X6A PACKAGE DIMENSIONS**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043
B	0.330	0.510	0.013	0.020
C	0.200	0.300	0.008	0.012
D	5.900	6.100	0.232	0.240
D1	3.380	3.780	0.133	0.149
D2	1.100		0.043	
E	5.700	5.800	0.224	0.228
e	1.270BSC.		1.270BSC.	
F	4.800	5.000	0.189	0.197
G	0.361	0.396	0.014	0.016
H	0.410	0.610	0.016	0.024
I	0.060	0.200	0.002	0.008
L	0.510	0.710	0.020	0.028
θ	0°	12°	0°	12°