

■ DESCRIPTION

The SMC4931M uses trench MOSFET technology. Provides extremely low $R_{DS(ON)}$, Low resistance package and excellent fast switching performance. This device is ideal for efficient and fast switching applications.

■ PART NUMBER INFORMATION

SMC 4931 M - TR G

a	b	c	d	e
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a : Company name.

b : Product Serial number.

c : Package code M:SOP-8

d : Handling code TR:Tape&Reel

e : Green produce code G:RoHS Compliant

■ FEATURES

$V_{DS}=-30V, I_D=-7A$

$R_{DS(ON)}=20m\Omega(\text{Typ.}) @ V_{GS}=-10V$

$R_{DS(ON)}=26m\Omega(\text{Typ.}) @ V_{GS}=-4.5V$

◆ 100% EAS Guarantee

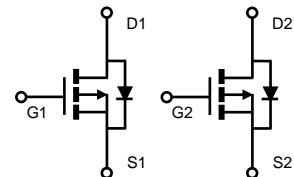
◆ High power and current handling capability

■ APPLICATIONS

◆ High Frequency Point-of-Load Synchronous

◆ DC-DC Power System

◆ Load Switch



SOP-8

■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$ Unless otherwise noted)

Symbol	Parameter	Rating	Units	
V_{DSS}	Drain-Source Voltage	-30	V	
V_{GSS}	Gate-Source Voltage	± 25	V	
I_D	Continuous Drain Current	$T_A=25^\circ\text{C}$ $T_A=70^\circ\text{C}$	-7 -5.6	A
I_{DM}	Pulsed Drain Current ^B	-28	A	
I_{AS}	Avalanche Current ^B	-20	A	
E_{AS}	Single Pulse Avalanche energy L=0.1mH ^B	20	mJ	
P_D	Power Dissipation ^A	$T_A=25^\circ\text{C}$ $T_A=70^\circ\text{C}$	2 1.3	W
T_J	Operation Junction Temperature	-55/150	°C	
T_{STG}	Storage Temperature Range	-55/150	°C	

■ THERMAL RESISTANCE

Symbol	Parameter	Typ	Max	Units
$R_{\theta JA}$	Thermal Resistance Junction to Ambient ^A	$t \leq 10\text{s}$	62	°C/W
	Thermal Resistance Junction to Ambient ^{AC}	Steady-State	90	

ELECTRICAL CHARACTERISTICS($T_A=25^\circ\text{C}$ Unless otherwise noted)

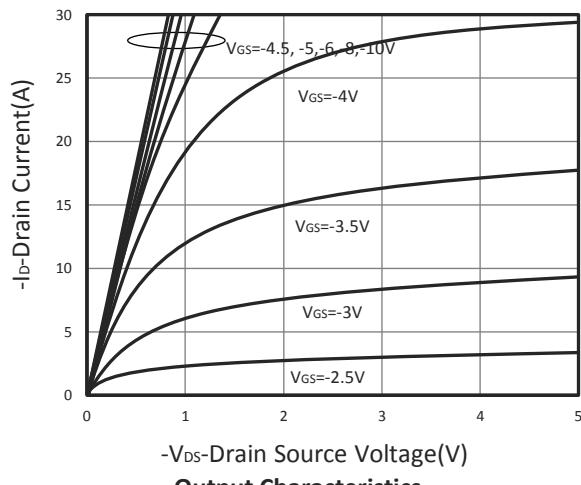
Symbol	Parameter	Condition	Min	Typ	Max	Unit	
Static Parameters							
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=-250\mu\text{A}$	-30			V	
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=-250\mu\text{A}$	-1	-1.6	-2.5	V	
I_{GSS}	Gate Leakage Current	$\text{V}_{\text{DS}}=0\text{V}, \text{V}_{\text{GS}}=\pm 20\text{V}$			± 100	nA	
I_{DSS}	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=-30\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{T}_J=25^\circ\text{C}$			-1	μA	
		$\text{V}_{\text{DS}}=-24\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{T}_J=75^\circ\text{C}$			-10		
$\text{R}_{\text{DS}(\text{ON})}$	Drain-source On-Resistance ^D	$\text{V}_{\text{GS}}=-10\text{V}, \text{I}_D=-7\text{A}$		20	25	$\text{m}\Omega$	
		$\text{V}_{\text{GS}}=-4.5\text{V}, \text{I}_D=-5.6\text{A}$		26	32		
G_{fs}	Forward Transconductance	$\text{V}_{\text{DS}}=-10\text{V}, \text{I}_D=-7\text{A}$		12.5		S	
Diode Characteristics							
V_{SD}	Diode Forward Voltage ^D	$\text{I}_S=-1\text{A}, \text{V}_{\text{GS}}=0\text{V}$			-1	V	
I_S	Diode Continuous Forward Current				-7	A	
t_{rr}	Reverse Recovery Time	$\text{I}_S=-7\text{A}, \frac{d\text{I}}{dt}=100\text{A}/\mu\text{s}$		11		ns	
Q_{rr}	Reverse Recovery Charge			5.8		nC	
Dynamic and Switching Parameters^E							
Q_g	Total Gate Charge	$\text{V}_{\text{DS}}=-15\text{V}, \text{V}_{\text{GS}}=-10\text{V}, \text{I}_D=-7\text{A}$		23.6	33	nC	
Q_g	Total Gate Charge (4.5V)			11.5	16.1		
Q_{gs}	Gate-Source Charge			4.2	5.9		
Q_{gd}	Gate-Drain Charge			4.4	6.2		
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=-15\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1\text{MHz}$		1280		pF	
C_{oss}	Output Capacitance			175			
C_{rss}	Reverse Transfer Capacitance			125			
$\text{t}_{\text{d}(\text{on})}$	Turn-On Time	$\text{V}_{\text{DD}}=-15\text{V}, \text{V}_{\text{GEN}}=-10\text{V}, \text{R}_G=3.3\Omega, \text{I}_D=-1\text{A}$		6.1	12	nS	
t_r				14	27		
$\text{t}_{\text{d}(\text{off})}$	Turn-Off Time			34	65		
t_f				13.2	25		

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

- A. Surface mounted on FR4 board using 1 in² pad size.
- B. Pulsed width limited by maximum junction temperature, $\text{T}_J(\text{MAX})=150^\circ\text{C}$ (initial temperature $\text{T}_J=25^\circ\text{C}$).
- C. Using $\leq 10\text{s}$ junction-to-ambient thermal resistance is base on $\text{T}_J(\text{MAX})=150^\circ\text{C}$.
- D. Pulse test width $\leq 300\mu\text{s}$ and duty cycle $\leq 2\%$.
- E. Guaranteed by design, not subject to production testing.

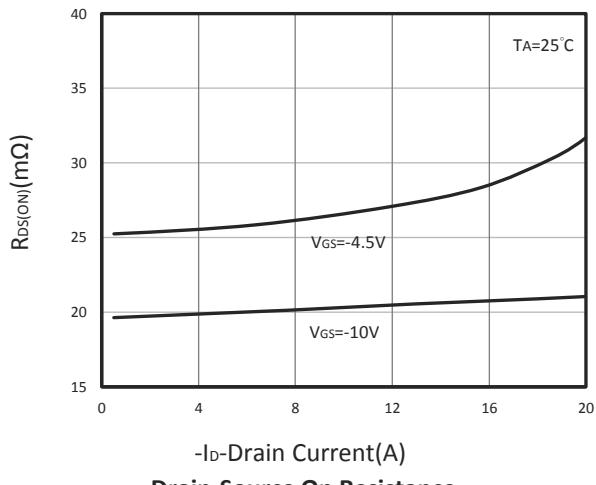
The products and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date. We assume no responsibility for any infringement of patents, patent rights, or other rights arising from the use of any information and circuitry in this datasheet.

■ TYPICAL CHARACTERISTICS



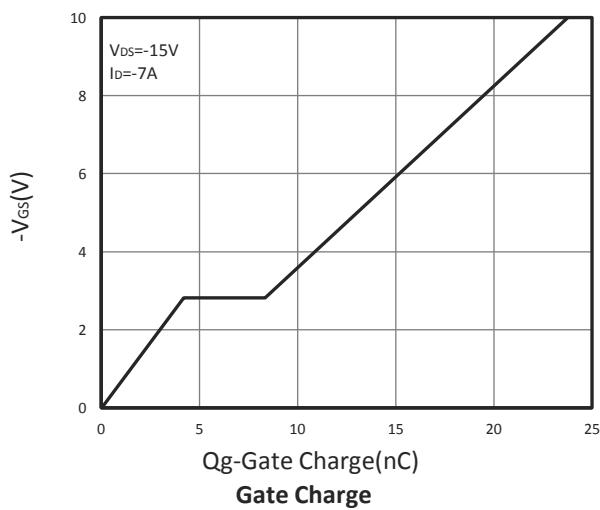
- V_{DS} -Drain Source Voltage(V)

Output Characteristics



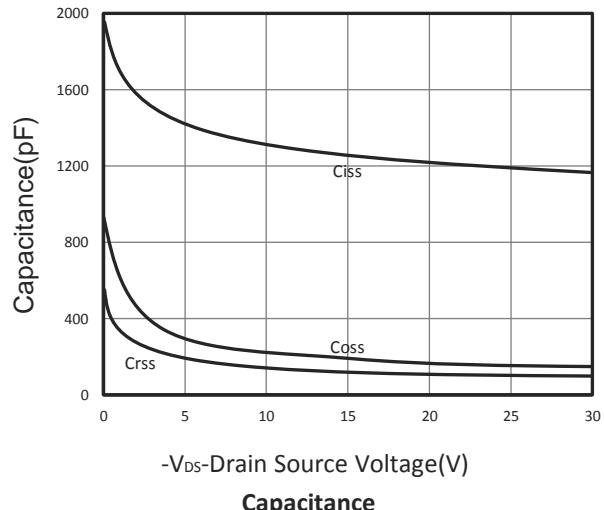
- I_D -Drain Current(A)

Drain-Source On Resistance



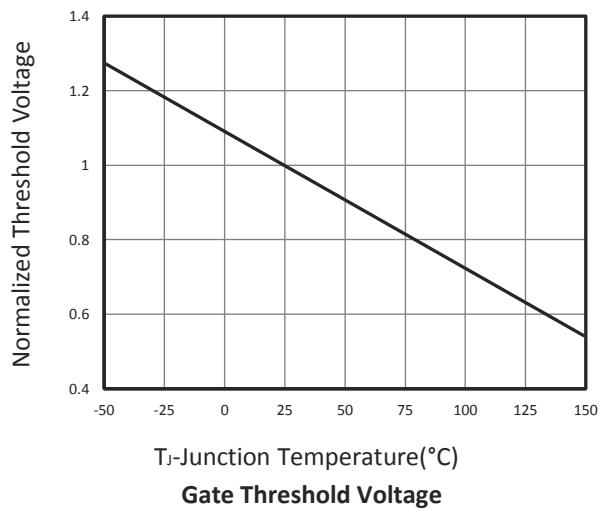
$V_{DS}=-15V$

$I_D=-7A$



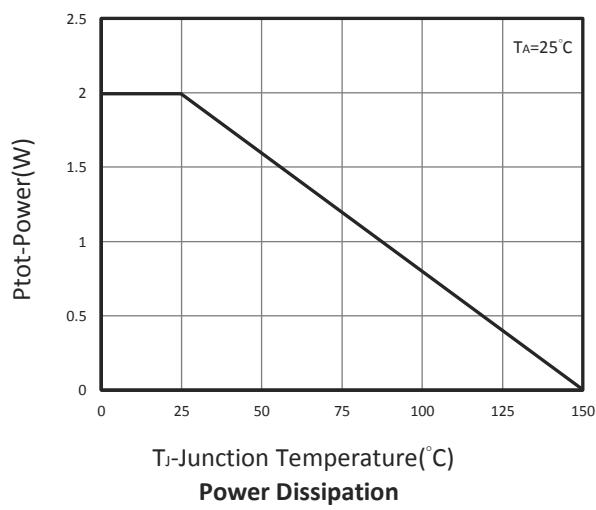
$-V_{DS}$ -Drain Source Voltage(V)

Capacitance



T_J -Junction Temperature($^{\circ}$ C)

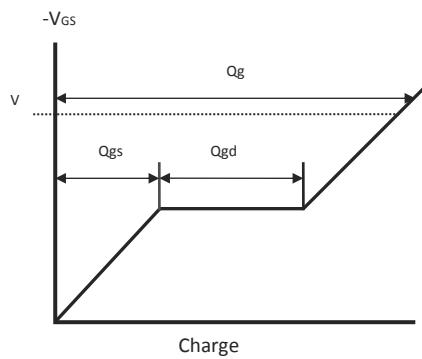
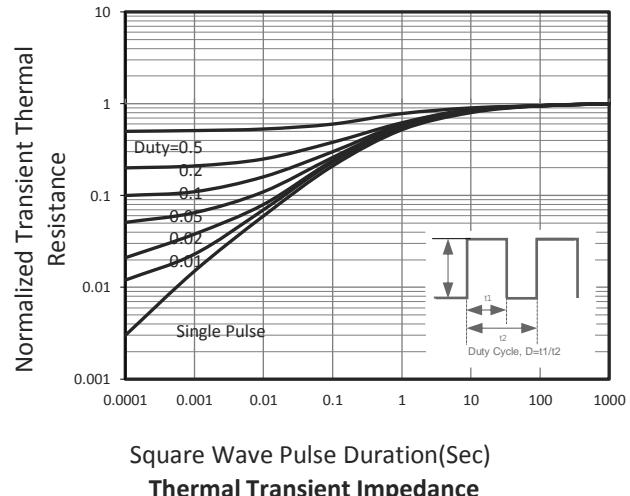
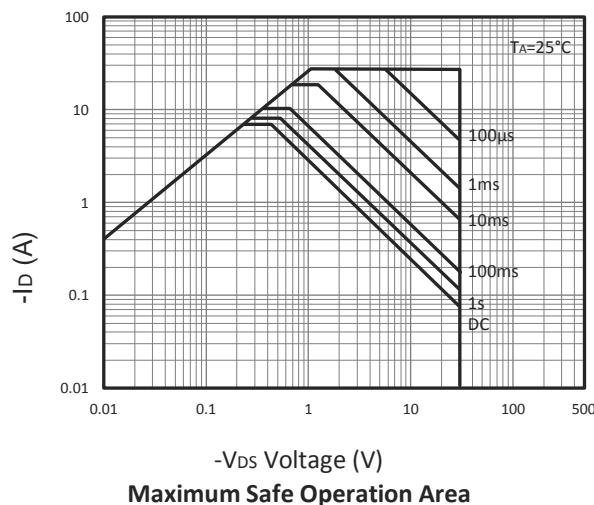
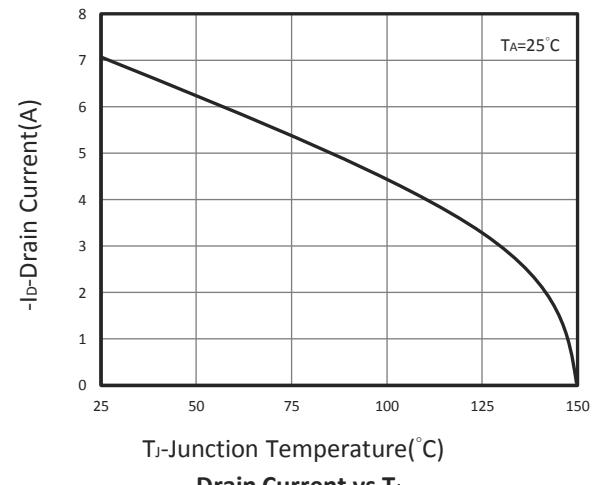
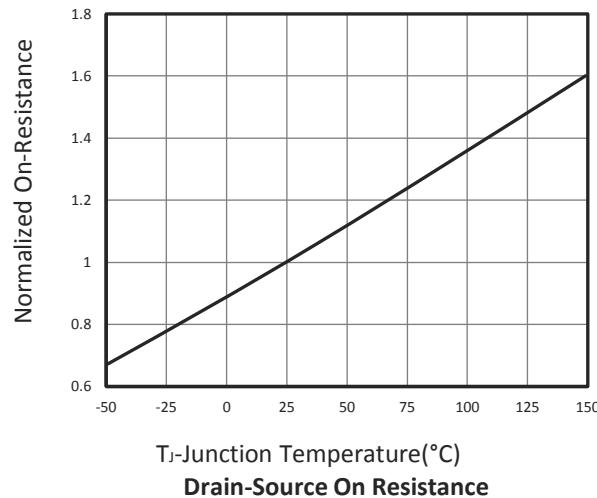
Gate Threshold Voltage



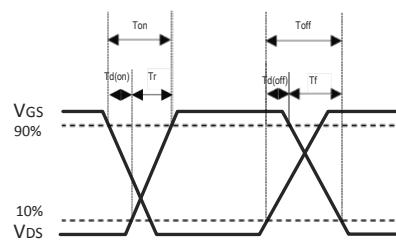
T_J -Junction Temperature($^{\circ}$ C)

Power Dissipation

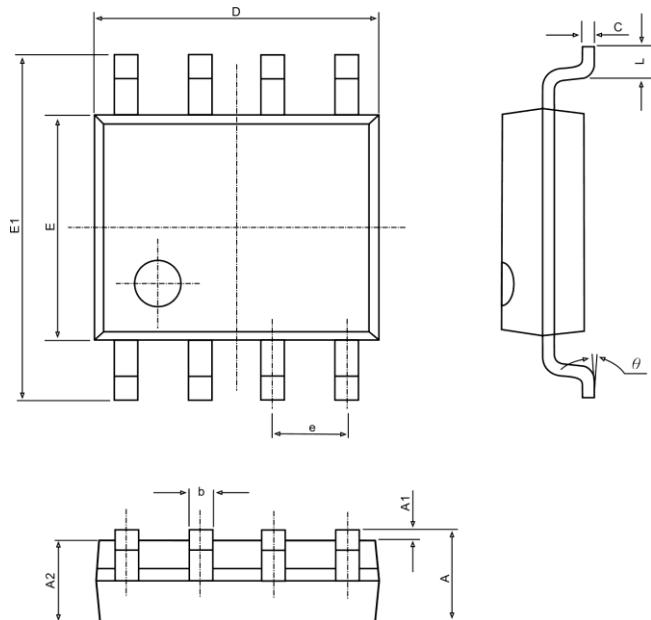
TYPICAL CHARACTERISTICS



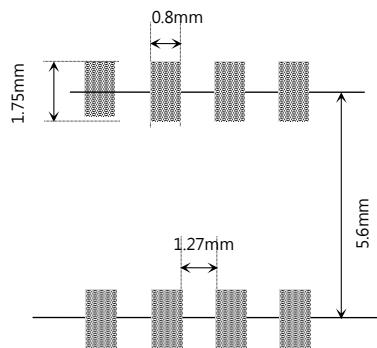
Gate Charge Waveform



SOP-8 PACKAGE DIMENSIONS



Recommended Land Pattern



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.300	1.500	0.051	0.059
b	0.390	0.490	0.015	0.019
c	0.200	0.250	0.008	0.010
D	4.800	5.100	0.189	0.201
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 BSC		0.050 BSC	
L	0.500	0.800	0.020	0.031
Θ	0°	8°	0°	8°