

Single N-Channel MOSFET

■ DESCRIPTION

SMC2868ESD used trench technology are well suited for high efficiency fast switching applications, this MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, this devices are well suited for applications in the small surface mount package.

■ PART NUMBER INFORMATION

SMC 2868 E SD - TR G

a	b	c	d	e	f
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a : Company name

b : Product Serial number

c : ESD Protection

d : Package code SD: SOT-323

e : Handling code TR: Tape&Reel

f : Green produce code G: RoHS Compliant

■ FEATURES

$V_{DS}=20V$, $I_D=0.9A$

$R_{DS(ON)}=200m\Omega$ (Typ.)@ $V_{GS}=4.5V$

$R_{DS(ON)}=245m\Omega$ (Typ.)@ $V_{GS}=2.5V$

$R_{DS(ON)}=310m\Omega$ (Typ.)@ $V_{GS}=1.8V$

$R_{DS(ON)}=380m\Omega$ (Typ.)@ $V_{GS}=1.5V$

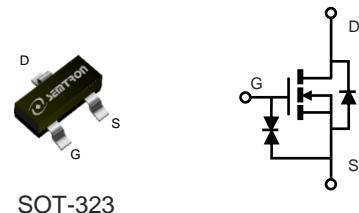
$R_{DS(ON)}=680m\Omega$ (Typ.)@ $V_{GS}=1.2V$

- ◆ High-speed switching, Low On-resistance
- ◆ 1.2V Low gate drive
- ◆ ESD protected

■ APPLICATIONS

◆ Hand-Held Instruments

◆ Switching application



■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ C$ Unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-Source Voltage	20	V
V_{GSS}	Gate-Source Voltage	± 8	V
I_D	Continuous Drain Current <small>$T_A=25^\circ C$</small>	0.9	A
	<small>$T_A=70^\circ C$</small>	0.73	A
I_{DM}	Pulsed Drain Current ^B	1.8	A
P_D	Power Dissipation ^A <small>$T_A=25^\circ C$</small>	0.36	W
	<small>$T_A=70^\circ C$</small>	0.23	W
T_J	Operation Junction Temperature	-55/150	$^\circ C$
T_{STG}	Storage Temperature Range	-55/150	$^\circ C$

■ THERMAL RESISTANCE

Symbol	Parameter	Typ	Max	Units
$R_{\theta JA}$	Thermal Resistance Junction to Ambient ^A $t \leq 10s$		-	$^\circ C/W$
	Thermal Resistance Junction to Ambient ^{AC} Steady-State		350	$^\circ C/W$

ELECTRICAL CHARACTERISTICS($T_A=25^\circ\text{C}$ Unless otherwise noted)

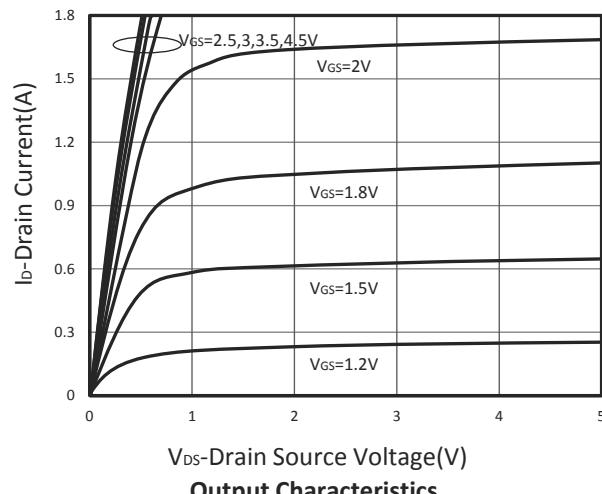
Symbol	Parameter	Condition	Min	Typ	Max	Unit	
Static Parameters							
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	20			V	
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	0.3	0.6	1	V	
I_{GSS}	Gate Leakage Current	$\text{V}_{\text{DS}}=0\text{V}, \text{V}_{\text{GS}}=\pm 8\text{V}$			± 10	μA	
I_{DSS}	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=20\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{T}_J=25^\circ\text{C}$			1	μA	
		$\text{V}_{\text{DS}}=12\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{T}_J=85^\circ\text{C}$			10		
$\text{R}_{\text{DS}(\text{ON})}$	Drain-source On-Resistance ^D	$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=0.9\text{A}$		200	270	$\text{m}\Omega$	
		$\text{V}_{\text{GS}}=2.5\text{V}, \text{I}_D=0.5\text{A}$		245	380		
		$\text{V}_{\text{GS}}=1.8\text{V}, \text{I}_D=0.3\text{A}$		310	500		
		$\text{V}_{\text{GS}}=1.5\text{V}, \text{I}_D=0.2\text{A}$		380	600		
		$\text{V}_{\text{GS}}=1.2\text{V}, \text{I}_D=0.1\text{A}$		680	1000		
G_{fs}	Forward Transconductance	$\text{V}_{\text{DS}}=5\text{V}, \text{I}_D=0.5\text{A}$		1.7		S	
Diode Characteristics							
V_{SD}	Diode Forward Voltage ^D	$\text{I}_S=0.2\text{A}, \text{V}_{\text{GS}}=0\text{V}$			1	V	
I_S	Diode Continuous Forward Current				0.46	A	
t_{rr}	Reverse Recovery Time	$\text{I}_S=0.5\text{A}, \frac{d\text{I}}{dt}=100\text{A}/\mu\text{s}$		8.8		ns	
Q_{rr}	Reverse Recovery Charge			0.8		nC	
Dynamic and Switching Parameters ^E							
Q_g	Total Gate Charge	$\text{V}_{\text{DS}}=10\text{V}, \text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=0.5\text{A}$		0.97		nC	
Q_{gs}	Gate-Source Charge			0.28			
Q_{gd}	Gate-Drain Charge			0.12			
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=10\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1\text{MHz}$		42		pF	
C_{oss}	Output Capacitance			9			
C_{rss}	Reverse Transfer Capacitance			6			
$\text{t}_{\text{d}(\text{on})}$	Turn-On Time	$\text{V}_{\text{DD}}=10\text{V}, \text{V}_{\text{GS}}=4.5\text{V}$ $\text{R}_G=6\Omega, \text{I}_D=0.5\text{A}$		6	11	nS	
t_r				3.8	7		
$\text{t}_{\text{d}(\text{off})}$	Turn-Off Time			14	23		
t_f				15	29		

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

- A. Surface mounted on FR4 board using 1 in² pad size.
- B. Pulsed width limited by maximum junction temperature, $\text{T}_J(\text{MAX})=150^\circ\text{C}$ (initial temperature $\text{T}_J=25^\circ\text{C}$).
- C. Using $\leq 10\text{s}$ junction-to-ambient thermal resistance is base on $\text{T}_J(\text{MAX})=150^\circ\text{C}$.
- D. Pulse test width $\leq 300\mu\text{s}$ and duty cycle $\leq 2\%$.
- E. Guaranteed by design, not subject to production testing.

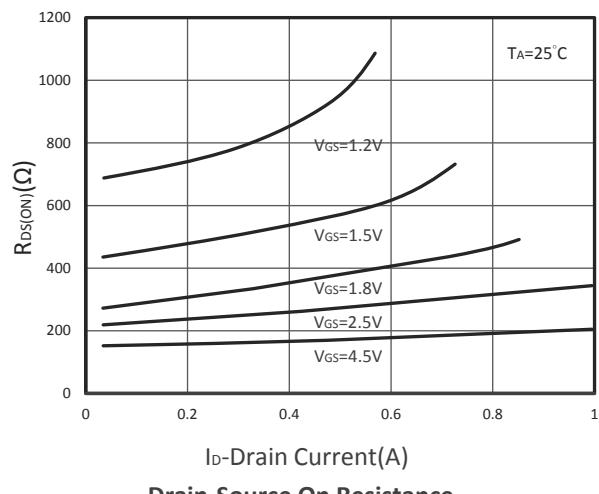
The products and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date. We assume no responsibility for any infringement of patents, patent rights, or other rights arising from the use of any information and circuitry in this datasheet.

■ TYPICAL CHARACTERISTICS



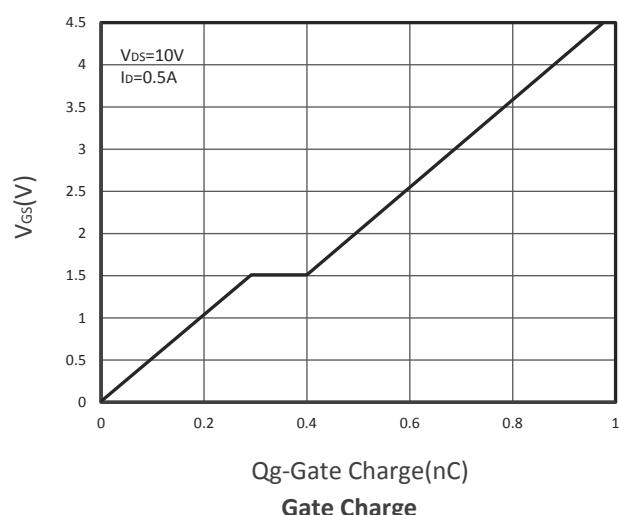
V_{DS}-Drain Source Voltage(V)

Output Characteristics



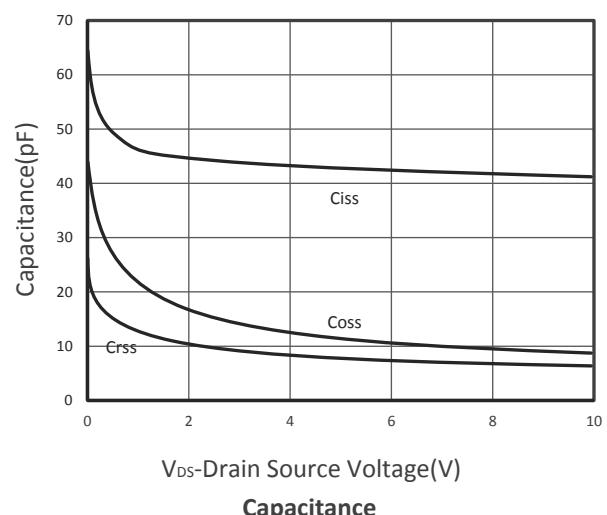
I_D -Drain Current(A)

Drain-Source On Resistance



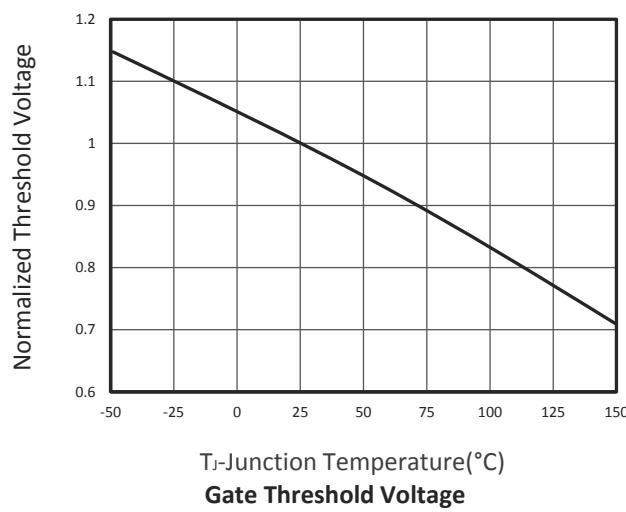
V_{DS} =10V

Gate Charge



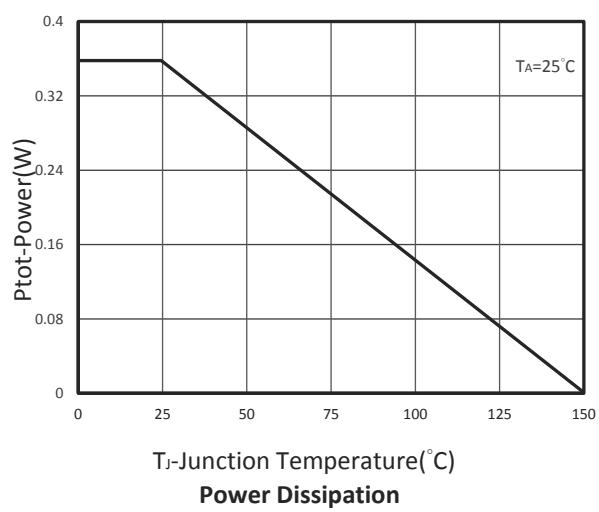
V_{DS} -Drain Source Voltage(V)

Capacitance



T_J -Junction Temperature(°C)

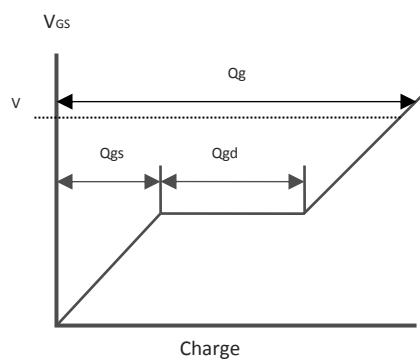
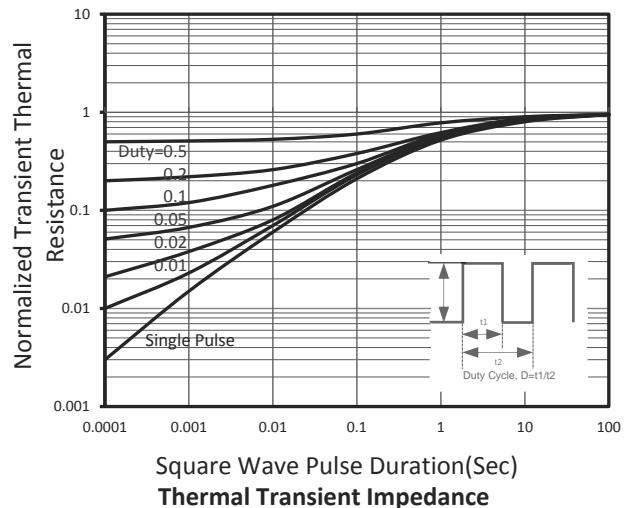
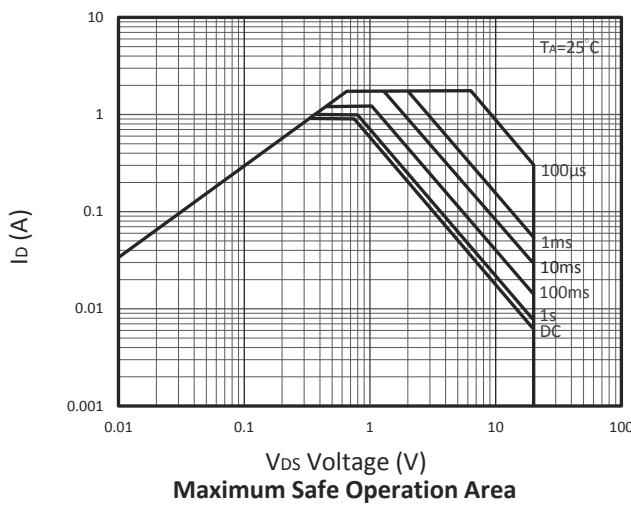
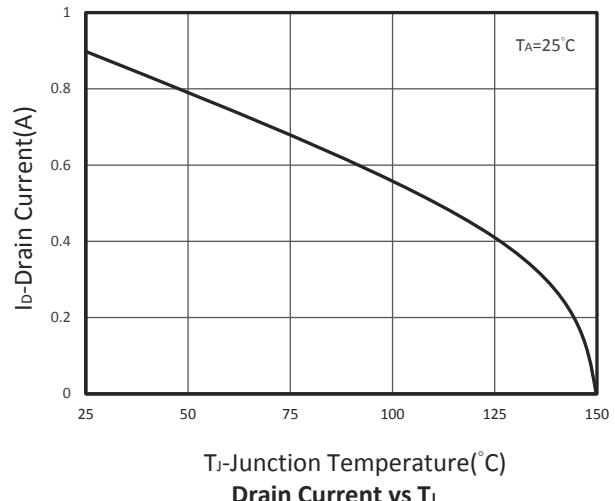
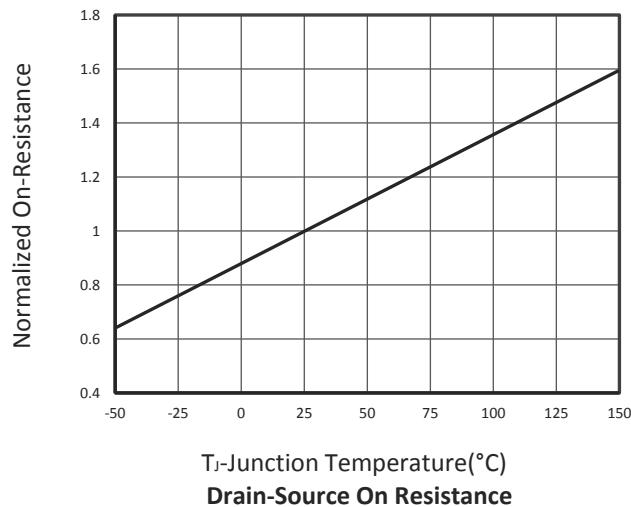
Gate Threshold Voltage



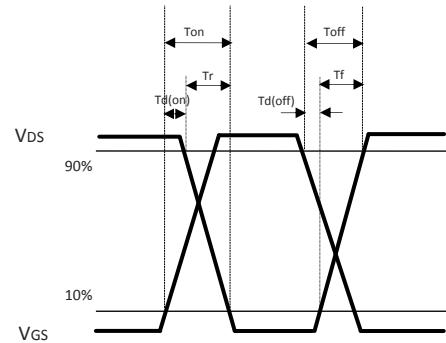
T_J -Junction Temperature(°C)

Power Dissipation

■ TYPICAL CHARACTERISTICS

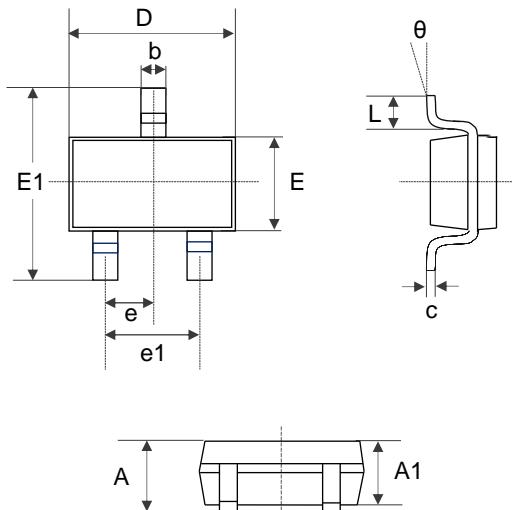


Gate Charge Waveform

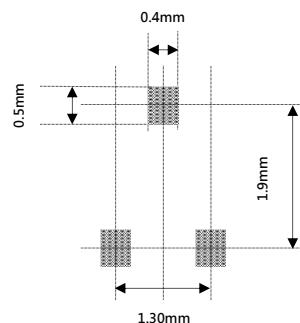


Switching Time Waveform

SOT-323 PACKAGE DIMENSIONS



Recommended Land Pattern



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043
A1	0.800	1.000	0.031	0.039
b	0.200	0.400	0.008	0.016
c	0.080	0.250	0.003	0.010
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.1500	2.450	0.085	0.096
e	0.650 BSC.		0.026 BSC.	
e1	1.200	1.400	0.047	0.055
L	0.15	0.45	0.06	0.018
θ	0°	8°	0°	8°