

## Dual N-Channel MOSFET

### ■ DESCRIPTION

SMC2208E is the dual N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced trench technology devices are well suited for high efficiency fast switching applications, low in-line power loss are needed in small outline surface mount package.

### ■ PART NUMBER INFORMATION

**SMC 2208 E CD - TR G**

a	b	c	d	e	f
---	---	---	---	---	---

a : Company name.

b : Product Serial number.

c : ESD

d : Package code      CD: SOT-363/SC70-6

e : Handling code      TR: Tape&Reel

f : Green produce code G: RoHS Compliant

### ■ FEATURES

**$V_{DS} = 20V, \quad I_D = 0.82A$**

$R_{DS(ON)}=195m\Omega(\text{Typ.}) @ V_{GS}=4.5V$

$R_{DS(ON)}=230m\Omega(\text{Typ.}) @ V_{GS}=2.5V$

$R_{DS(ON)}=300m\Omega(\text{Typ.}) @ V_{GS}=1.8V$

$R_{DS(ON)}=355m\Omega(\text{Typ.}) @ V_{GS}=1.5V$

$R_{DS(ON)}=580m\Omega(\text{Typ.}) @ V_{GS}=1.2V$

◆ ESD protected

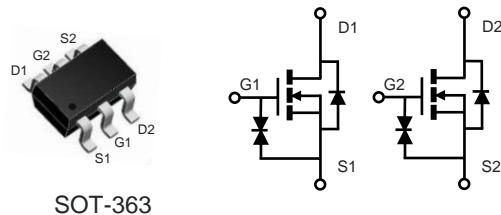
◆ Fast switching

◆ 1.2V Low gate drive applications

### ■ APPLICATIONS

◆ Hand-Held Instruments

◆ Low current DC/DC Applications



### ■ ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ Unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-Source Voltage	20	V
$V_{GSS}$	Gate-Source Voltage	$\pm 8$	V
$I_D$	Continuous Drain Current	$T_A=25^\circ\text{C}$	0.82
		$T_A=70^\circ\text{C}$	0.65
$I_{DM}$	Pulsed Drain Current <sup>A</sup>	3.3	A
$P_D$	Power Dissipation <sup>B</sup>	$T_A=25^\circ\text{C}$	0.3
		$T_A=70^\circ\text{C}$	0.19
$T_J$	Operation Junction Temperature	-55/150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55/150	$^\circ\text{C}$

### ■ THERMAL RESISTANCE

Symbol	Parameter	Typ	Max	Units
$R_{\theta JA}$	Thermal Resistance Junction to Ambient <sup>B</sup>	$t \leq 10\text{s}$	420	$^\circ\text{C}/\text{W}$
	Thermal Resistance Junction to Ambient <sup>BC</sup>	Steady-State	460	

**ELECTRICAL CHARACTERISTICS( $T_A = 25^\circ\text{C}$  Unless otherwise noted)**

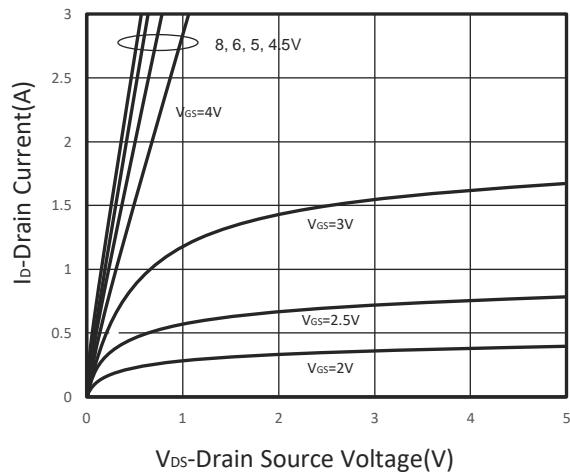
Symbol	Parameter	Condition	Min	Typ	Max	Unit	
<b>Static Parameters</b>							
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	20			V	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.3	0.6	1	V	
$I_{GSS}$	Gate Leakage Current	$V_{DS}=0\text{V}, V_{GS}=\pm 8\text{V}$			$\pm 20$	$\mu\text{A}$	
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=20\text{V}, V_{GS}=0\text{V}, T_J=25^\circ\text{C}$		1		$\mu\text{A}$	
		$V_{DS}=16\text{V}, V_{GS}=0\text{V}, T_J=75^\circ\text{C}$		10			
$R_{DS(\text{ON})}$	Drain-source On-Resistance	$V_{GS}=4.5\text{V}, I_D=0.82\text{A}$		195	270	$\text{m}\Omega$	
		$V_{GS}=2.5\text{V}, I_D=0.6\text{A}$		230	320		
		$V_{GS}=1.8\text{V}, I_D=0.3\text{A}$		300	400		
		$V_{GS}=1.5\text{V}, I_D=0.2\text{A}$		355	500		
		$V_{GS}=1.5\text{V}, I_D=0.1\text{A}$		580	800		
$G_f$	Forward Transconductance	$V_{DS}=5\text{V}, I_D=0.82\text{A}$		3.8		S	
<b>Diode Characteristics</b>							
$V_{SD}$	Diode Forward Voltage	$I_S=0.5\text{A}, V_{GS}=0\text{V}$			1	V	
$I_S$	Diode Continuous Forward Current				0.6	A	
<b>Dynamic and Switching Parameters</b>							
$Q_g$	Total Gate Charge	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}, I_D=0.5\text{A}$		1.4	2	nC	
$Q_{gs}$	Gate-Source Charge			0.17	0.24		
$Q_{gd}$	Gate-Drain Charge			0.3	0.42		
$C_{iss}$	Input Capacitance	$V_{DS}=10\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$		50		pF	
$C_{oss}$	Output Capacitance			18			
$C_{rss}$	Reverse Transfer Capacitance			10			
$t_{d(on)}$	Turn-On Time	$V_{DD}=10\text{V}, V_{GEN}=4.5\text{V}, R_G=10\Omega, I_D=0.5\text{A}$		4.6	9	nS	
$t_r$				3.2	6		
$t_{d(off)}$	Turn-Off Time			12.5	24		
$t_f$				5	10		

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

- A. Pulsed width limited by maximum junction temperature,  $T_J(\text{MAX})=150^\circ\text{C}$ .
- B. Measure the value in a still air environment at  $T_A=25^\circ\text{C}$ , using an installation mounted on a 1 in2 FR-4 board, maximum junction temperature  $T_J(\text{MAX})=150^\circ\text{C}$ .
- C.  $T_J(\text{MAX})=150^\circ\text{C}$ , using junction-to-case thermal resistance ( $R_{\thetaJC}$ ) is more useful in additional heat sinking is used.

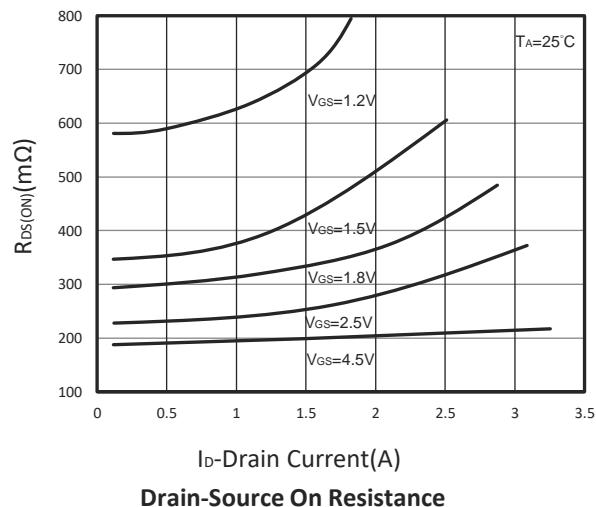
The products and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date. We assume no responsibility for any infringement of patents, patent rights, or other rights arising from the use of any information and circuitry in this datasheet.

## ■ TYPICAL CHARACTERISTICS



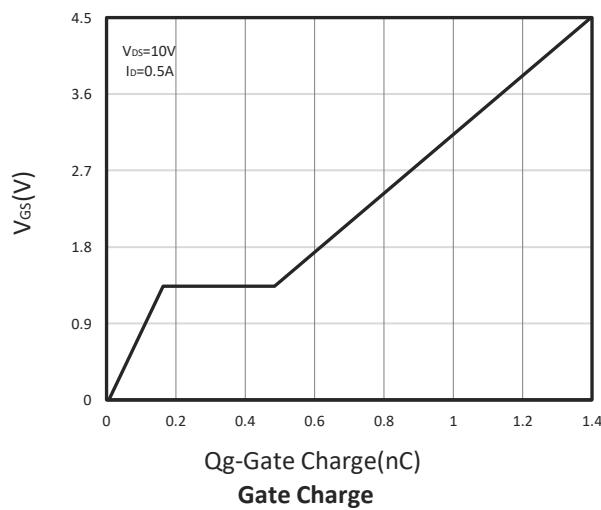
V<sub>DS</sub>-Drain Source Voltage(V)

Output Characteristics



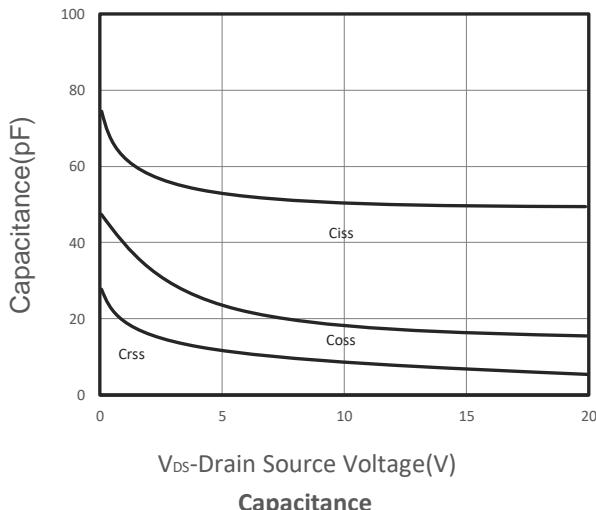
I<sub>D</sub>-Drain Current(A)

Drain-Source On Resistance



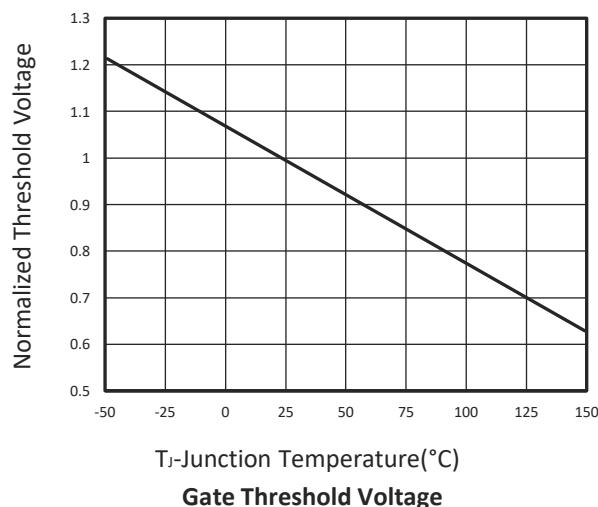
Q<sub>g</sub>-Gate Charge(nC)

Gate Charge



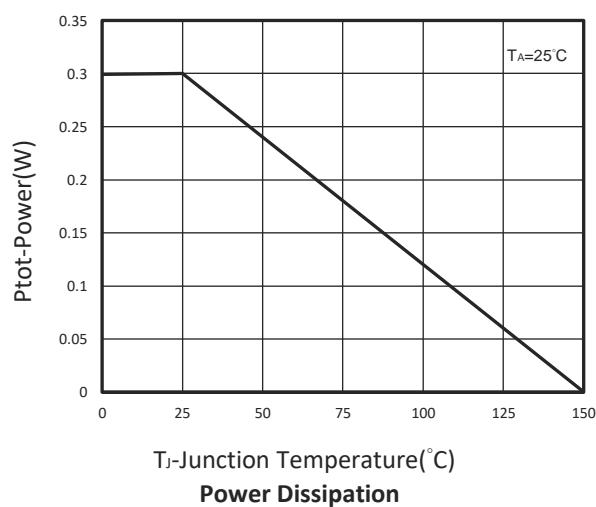
V<sub>DS</sub>-Drain Source Voltage(V)

Capacitance



T<sub>J</sub>-Junction Temperature(°C)

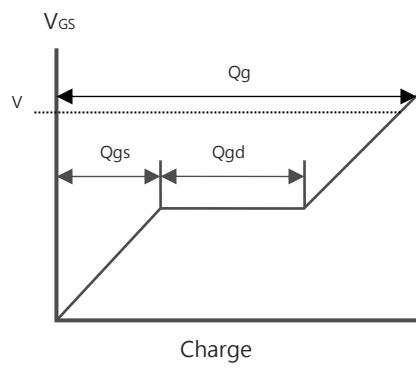
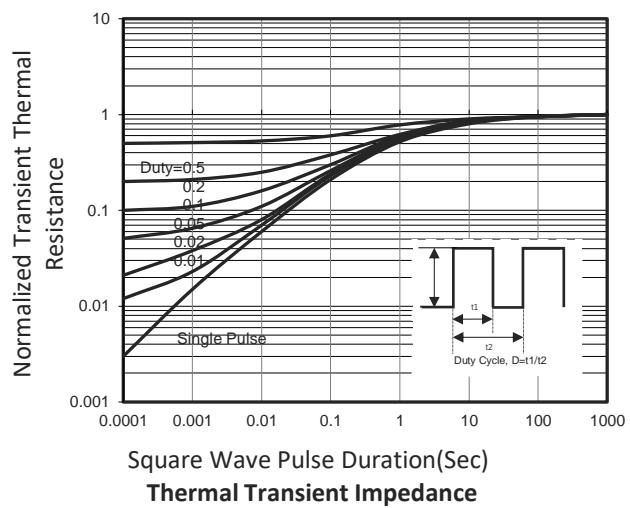
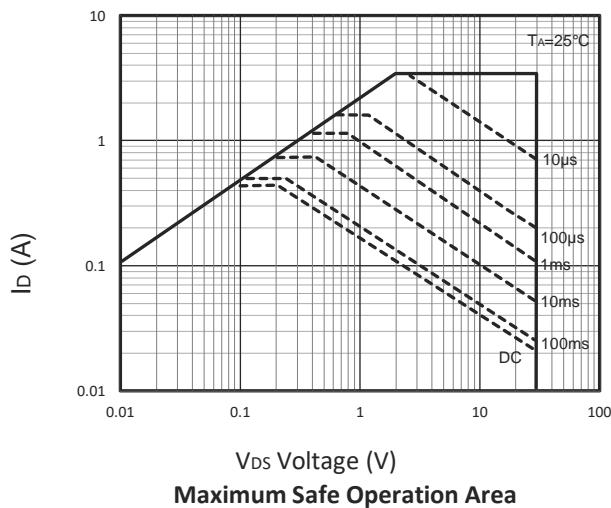
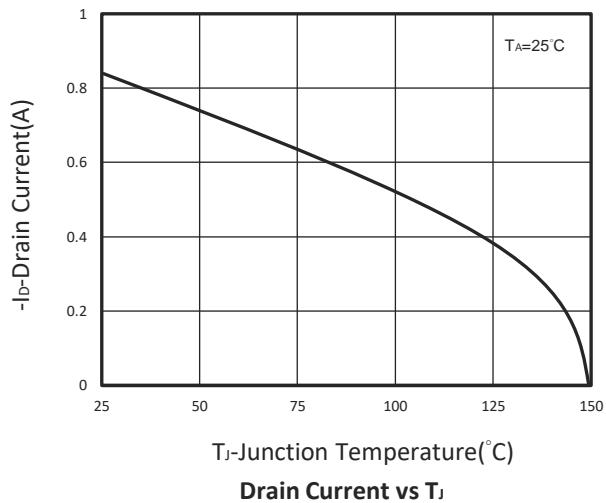
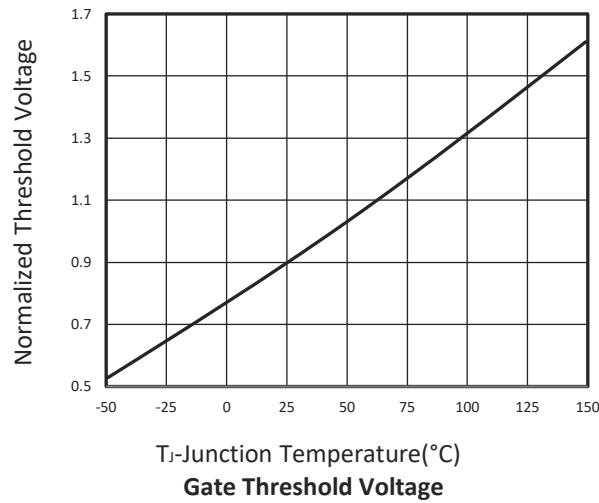
Gate Threshold Voltage



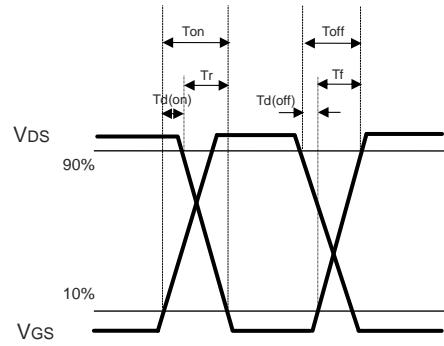
T<sub>J</sub>-Junction Temperature(°C)

Power Dissipation

## ■ TYPICAL CHARACTERISTICS

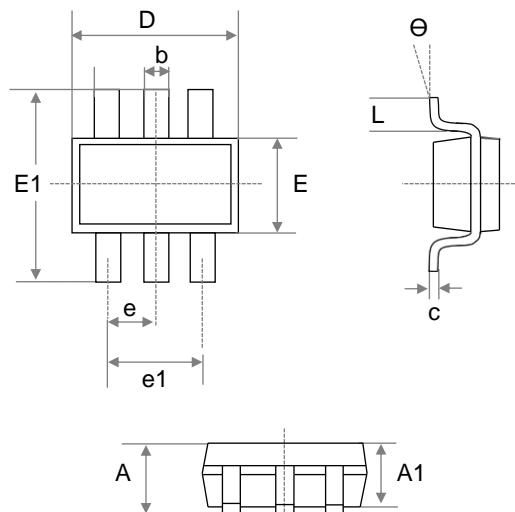


Gate Chrgre Waveform

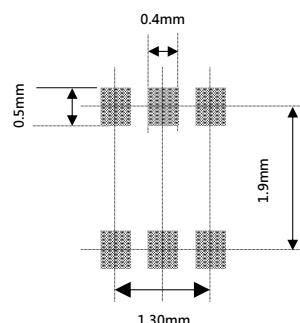


Switching Time Waveform

## SOT-363 PACKAGE DIMENSIONS



**Recommended Land Pattern**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.800	1.100	0.031	0.043
A1	0.800	1.000	0.031	0.039
b	0.100	0.330	0.004	0.013
c	0.100	0.250	0.004	0.010
D	1.800	2.200	0.071	0.087
E	1.150	1.350	0.053	0.045
E1	1.800	2.400	0.071	0.094
e	0.650 BSC.		0.026 BSC.	
e1	1.300 BSC.		0.052 BSC.	
L	0.100	0.350	0.004	0.014
Θ	0°	8°	0°	8°