

20V N-Channel Enhancement Mode MOSFET

■ DESCRIPTION

The SMC8205AW is the Dual N-Channel logic enhancement mode power field effect transistor which is produced using high cell density advanced trench technology to provide excellent $R_{DS(ON)}$.

This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application , and low in-line power loss are needed in small outline surface mount package.

SMC8205AW-TRG ROHS Compliant This is Halogen Free

■ FEATURE

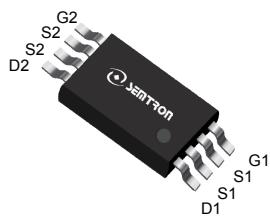
- ◆ 20V/6.0A, $R_{DS(ON)} = 21m\Omega(\text{typ.}) @ V_{GS} = 4.5V$
- ◆ 20V/5.2A, $R_{DS(ON)} = 25m\Omega(\text{typ.}) @ V_{GS} = 2.5V$
- ◆ Super high density cell design for extremely low $R_{DS(ON)}$
- ◆ Exceptional on-resistance and Maximum DC current capability

■ APPLICATIONS

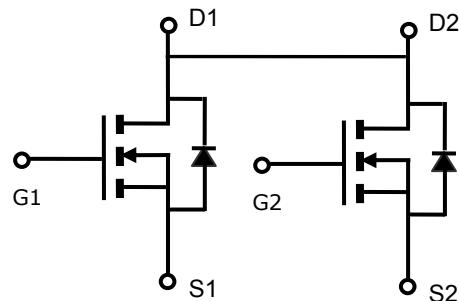
- ◆ Power Management in Note book
- ◆ Portable Equipment
- ◆ Battery Powered System



■ PIN CONFIGURATION



TSSOP-8
Top View



■ PART NUMBER INFORMATION

<u>SMC</u> <u>8205A</u> <u>W</u> - <u>TR</u> <u>G</u> a b c d e	a : Company name. b : Product Serial number. c : Package code d : Handling code e : Green produce code
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■ ORDERING INFORMATION

Part Number	Package Code	Handling Code	Shipping
SMC8205AW-TRG	W : TSSOP-8	TR : Tape&Reel	3K/Reel

※ Year Code : 0 ~ 9, 2010 : 0

※ Week Code : A(1~2) ~ Z(53~54)

※ TSSOP-8 : Only available in tape and reel packaging.

■ ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless otherwise noted)

Symbol	Parameter		Typical	Unit
V_{DSS}	Drain-Source Voltage		20	V
V_{GSS}	Gate-Source Voltage		± 12	V
I_D	Continuous Drain Current ($T_c=25^\circ C$) ^A	$V_{GS}=4.5V$	6.0	A
	Continuous Drain Current ($T_c=70^\circ C$)		5.0	A
I_{DM}	Pulsed Drain Current ^B		20	A
P_D	Power Dissipation	$T_A=25^\circ C$ $T_A=70^\circ C$	1.5 1.0	W
T_J	Operation Junction Temperature		-55 to 150	$^\circ C$
T_{STG}	Storage Temperature Range		-55 to 150	$^\circ C$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient ^A Steady-State	-	100	$^\circ C/W$
$R_{\theta JL}$	Thermal Resistance Junction to Lead ^A Steady-State	-	75	$^\circ C/W$

ELECTRICAL CHARACTERISTICS($T_J = 25^\circ C$ Unless otherwise noted)

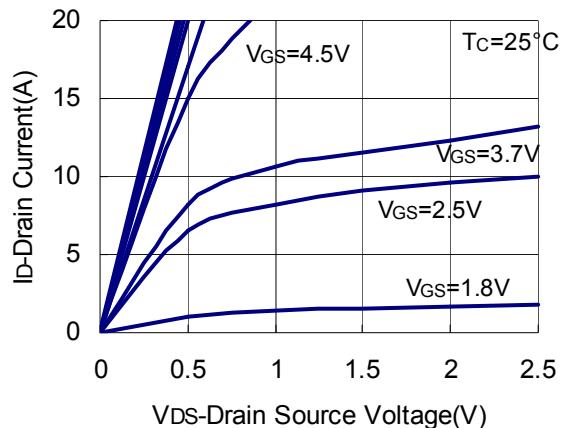
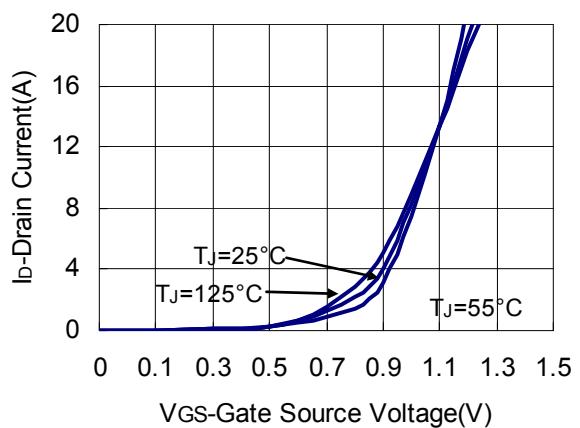
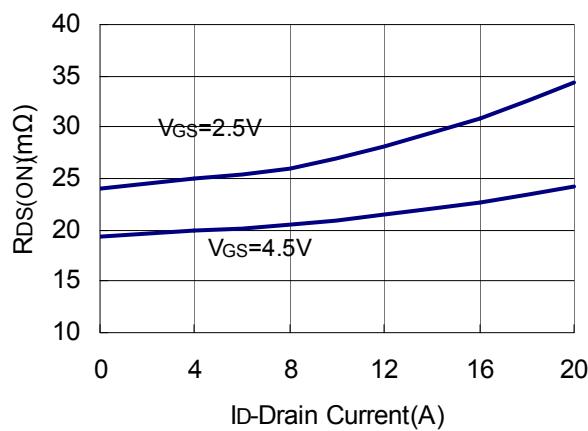
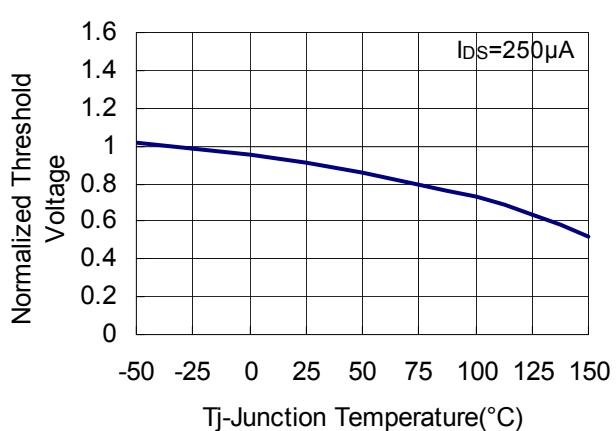
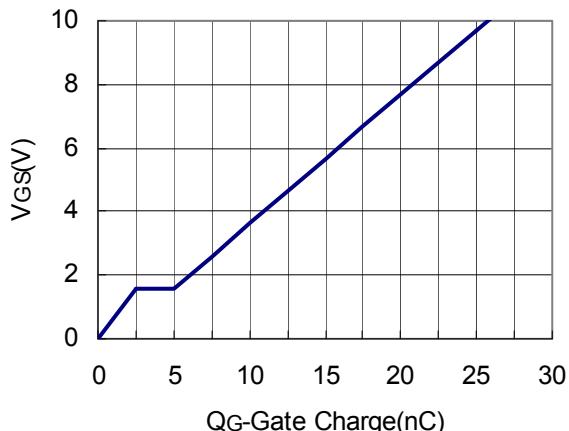
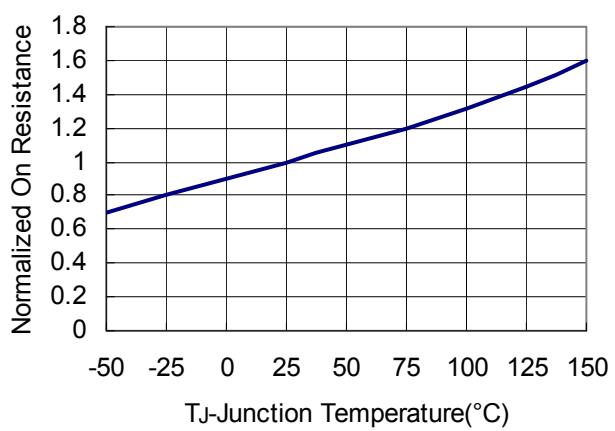
Symbol	Parameter	Condition	Min	Typ	Max	Unit	
Static Parameters							
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20			V	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	0.7	1.2	V	
I_{GSS}	Gate Leakage Current	$V_{DS}=0V, V_{GS}=\pm 12V$			± 100	nA	
I_{DSS}	Zero Gate Voltage, Drain-Source Leakage Current	$V_{DS}=16V, V_{GS}=0V$ $T_J=25^\circ C$			1	μA	
		$V_{DS}=16V, V_{GS}=0V$ $T_J=55^\circ C$			5		
$R_{DS(ON)}$	Drain-source On-Resistance ^B	$V_{GS}=4.5V, I_D=6.0A$ $V_{GS}=2.5V, I_D=5.2A$	21 25	25 28		$m\Omega$	
G_f	Forward Transconductance	$V_{DS}=15V, I_D=3.6A$		10		S	
Source-Drain Diode							
V_{SD}	Diode Forward Voltage	$I_S=1.0A, V_{GS}=0V$		0.75	1.0	V	
I_S	Continuous Source Current ^{AD}				10	A	
Dynamic Parameters							
$Q_g (4.5V)$	Total Gate Charge	$V_{DS}=10V$ $V_{GS}=4.5V$ $I_D=6.0A$		17		nC	
Q_{gs}	Gate-Source Charge			0.7			
Q_{gd}	Gate-Drain Charge			3.4			
C_{iss}	Input Capacitance	$V_{DS}=10V$ $V_{GS}=0V$ $f=1MHz$		850		pF	
C_{oss}	Output Capacitance			142			
C_{rss}	Reverse Transfer Capacitance			112			
$t_{d(on)}$	Turn-On Time	$V_{DD}=10V$ $I_D=6A$ $V_{GEN}=4.5V$ $R_G=3.3\Omega$		6.5		nS	
t_r				12			
$t_{d(off)}$	Turn-Off Time			32			
t_f				7.3			

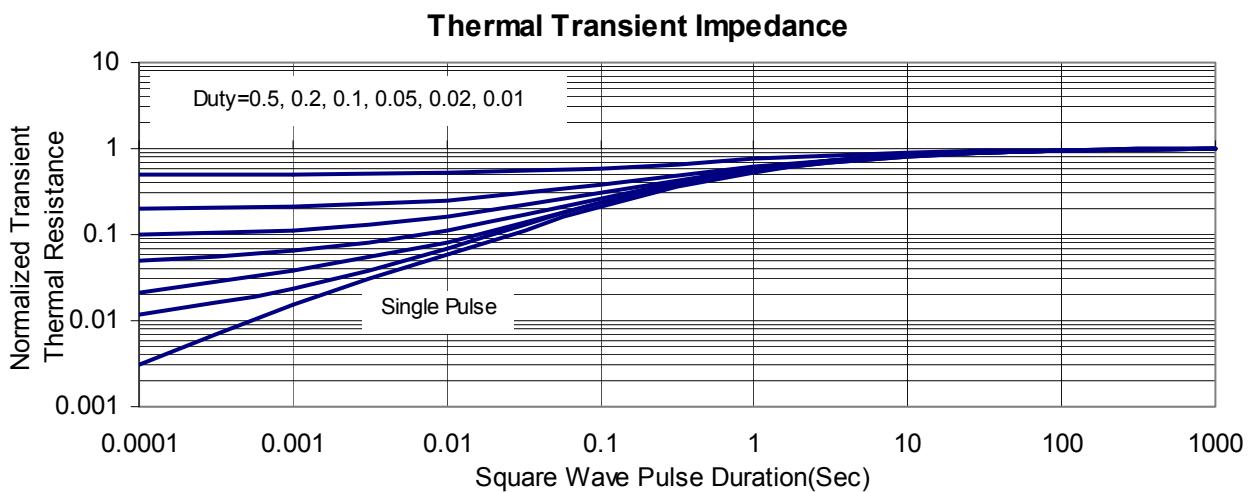
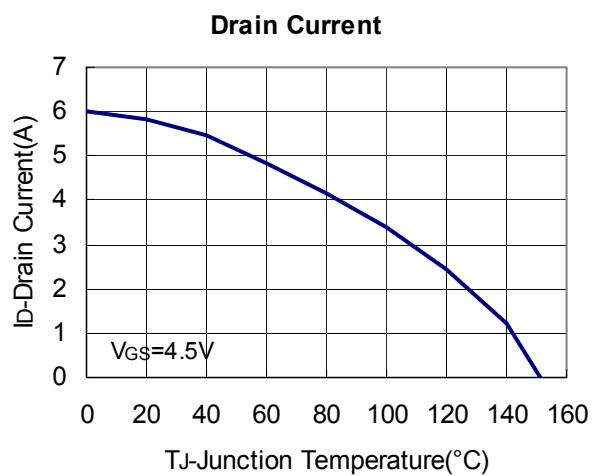
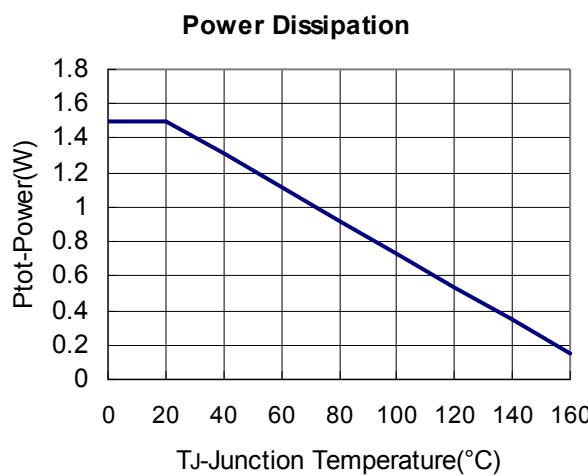
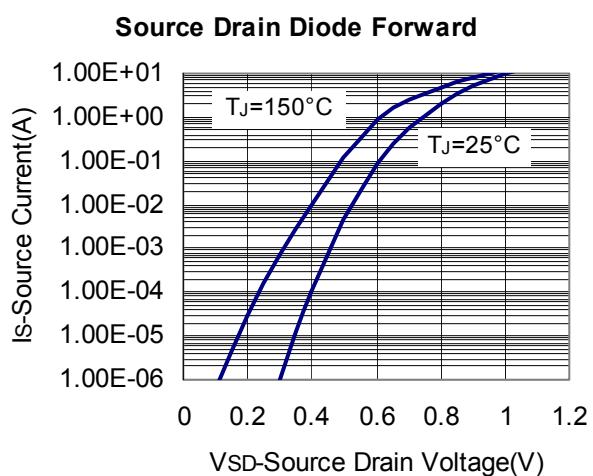
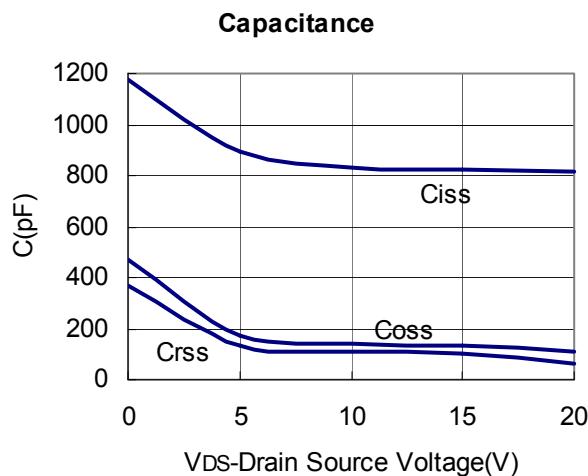
Note:

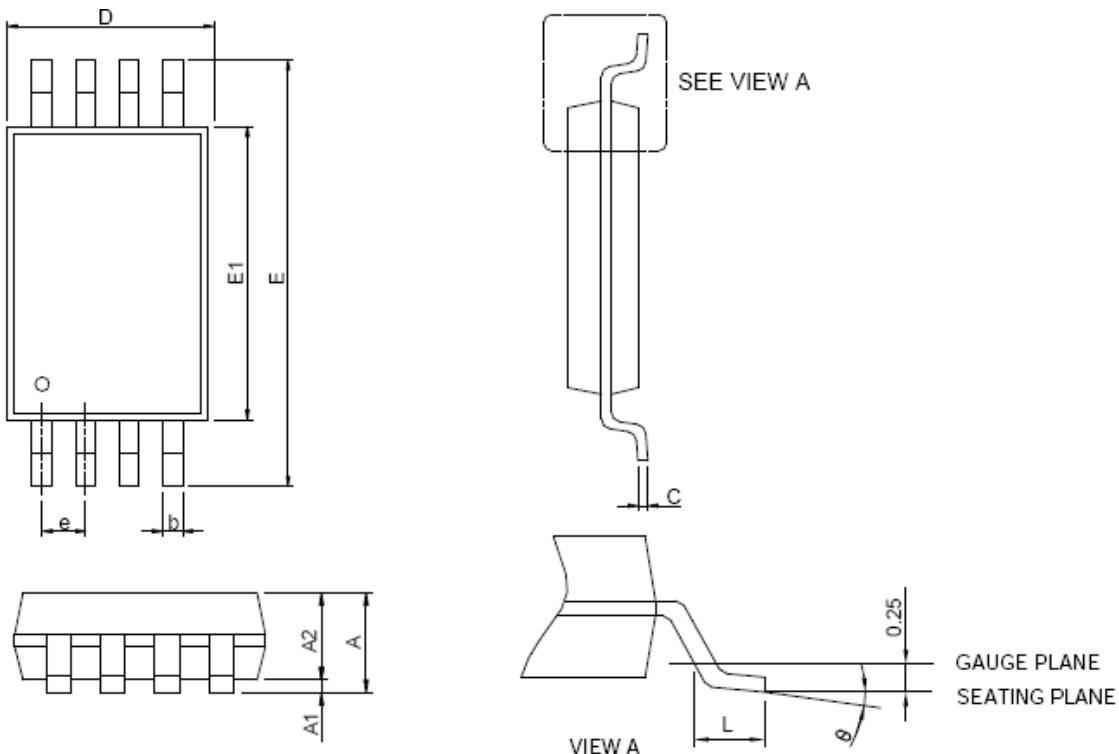
- A. The value of R_{SDA} is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_c=25^\circ C$.
- B. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- C. The EAS data shows Max. rating. The test condition is $V_{DD}=-25V, V_{GS}=-10V, L=0.1mH$.
- D. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

The products and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date

We assume no responsibility for any infringement of patents, patent rights, or other rights arising from the use of any information and circuitry in this datasheet.

TYPICAL CHARACTERISTICS (25°C Unless Note)
Output Characteristics

Drain-Source On Resistance

Transfer Characteristics

Gate Threshold Voltage

Gate Charge

Drain Source On Resistance


TYPICAL CHARACTERISTICS (25°C Unless Note)


TSSOP-8 PACKAGE DIMENSIONS


SYMBOL	TSSOP-8			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A		1.20		0.047
A1	0.05	0.15	0.002	0.006
A2	0.80	1.05	0.031	0.041
b	0.19	0.30	0.007	0.012
c	0.09	0.20	0.004	0.008
D	2.90	3.10	0.114	0.122
E	6.20	6.60	0.244	0.260
E1	4.30	4.50	0.169	0.177
e	0.65 BSC		0.026 BSC	
L	0.45	0.75	0.018	0.030
θ	0 °	8 °	0 °	8 °