

Single N-Channel MOSFET

■ DESCRIPTION

SMC6276ESA used trench technology are well suited for high efficiency fast switching applications, this MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, this devices are well suited for applications in the small surface mount package.

■ PART NUMBER INFORMATION

SMC 6276 E SA - TR G

a	b	c	d	e	f
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a : Company name

b : Product Serial number

c : ESD Protection

d : Package code SA: SOT-723

e : Handling code TR: Tape&Reel

f : Green produce code G: RoHS Compliant

■ FEATURES

$V_{DS}=60V$, $I_D=0.2A$

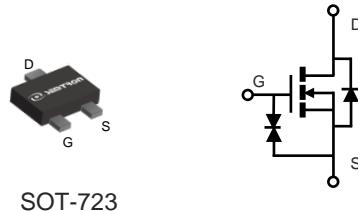
$R_{DS(ON)}=1.5\Omega$ (Typ.)@ $V_{GS}=10V$

$R_{DS(ON)}=2.0\Omega$ (Typ.)@ $V_{GS}=4.5V$

- ◆ High Speed Switching
- ◆ ESD Protection > $\pm 2KV$ HBM

■ APPLICATIONS

- ◆ Load Switch
- ◆ Portable appliances



■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ C$ Unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-Source Voltage	60	V
V_{GSS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current $T_A=25^\circ C$	0.2	A
		$T_A=70^\circ C$	0.16
I_{DM}	Pulsed Drain Current ^B	0.8	A
P_D	Power Dissipation ^A $T_A=25^\circ C$	0.15	W
		$T_A=70^\circ C$	0.1
T_J	Operation Junction Temperature	-55/150	$^\circ C$
T_{STG}	Storage Temperature Range	-55/150	$^\circ C$

■ THERMAL RESISTANCE

Symbol	Parameter	Typ	Max	Units
$R_{\theta JA}$	Thermal Resistance Junction to Ambient ^A $t \leq 10s$		-	$^\circ C/W$
	Thermal Resistance Junction to Ambient ^{AC} Steady-State		833	$^\circ C/W$

ELECTRICAL CHARACTERISTICS($T_A=25^\circ\text{C}$ Unless otherwise noted)

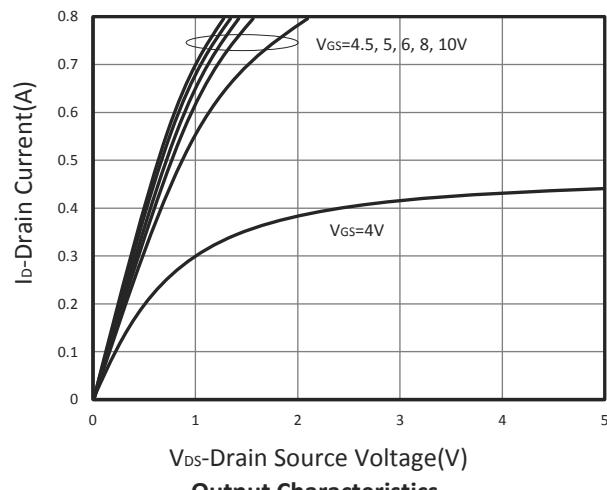
Symbol	Parameter	Condition	Min	Typ	Max	Unit	
Static Parameters							
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	60			V	
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1	1.6	2.5	V	
I_{GSS}	Gate Leakage Current	$\text{V}_{\text{DS}}=0\text{V}, \text{V}_{\text{GS}}=\pm 20\text{V}$			± 10	μA	
I_{DSS}	Zero Gate Voltage Drain Current	$\text{V}_{\text{DS}}=20\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{T}_J=25^\circ\text{C}$			1	μA	
		$\text{V}_{\text{DS}}=12\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{T}_J=85^\circ\text{C}$			30		
$\text{R}_{\text{DS}(\text{ON})}$	Drain-source On-Resistance ⁸	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=0.2\text{A}$		1.5	2	Ω	
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=0.15\text{A}$		2	3		
Diode Characteristics							
V_{SD}	Diode Forward Voltage ^D	$\text{I}_S=0.2\text{A}, \text{V}_{\text{GS}}=0\text{V}$			1	V	
I_S	Diode Continuous Forward Current				0.1	A	
t_{rr}	Reverse Recovery Time	$\text{I}_S=0.4\text{A}, \frac{d\text{I}}{dt}=100\text{A}/\mu\text{s}$		40		ns	
Q_{rr}	Reverse Recovery Charge			39		nC	
Dynamic and Switching Parameters^E							
Q_g	Total Gate Charge	$\text{V}_{\text{DS}}=15\text{V}, \text{V}_{\text{GS}}=10\text{V}$ $\text{I}_D=0.4\text{A}$		0.65		nC	
Q_{gs}	Gate-Source Charge			0.1			
Q_{gd}	Gate-Drain Charge			0.21			
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1\text{MHz}$		31		pF	
C_{oss}	Output Capacitance			4.2			
C_{rss}	Reverse Transfer Capacitance			3			
$\text{t}_{\text{d}(\text{on})}$	Turn-On Time	$\text{V}_{\text{DD}}=30\text{V}, \text{V}_{\text{GS}}=10\text{V},$ $\text{R}_G=25\Omega, \text{I}_D=0.4$		3.8	7	nS	
t_r				3.6	6.8		
$\text{t}_{\text{d}(\text{off})}$	Turn-Off Time			16	30		
t_f				10	19		

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

- A. Surface mounted on FR4 board using the minimum recommended pad size.
- B. Pulsed width limited by maximum junction temperature, $\text{T}_J(\text{MAX})=150^\circ\text{C}$.
- C. Using $\leq 10\text{s}$ junction-to-ambient thermal resistance is base on $\text{T}_J(\text{MAX})=150^\circ\text{C}$.
- D. Pulse test width $\leq 300\mu\text{s}$ and duty cycle $\leq 2\%$.
- E. Guaranteed by design, not subject to production testing.

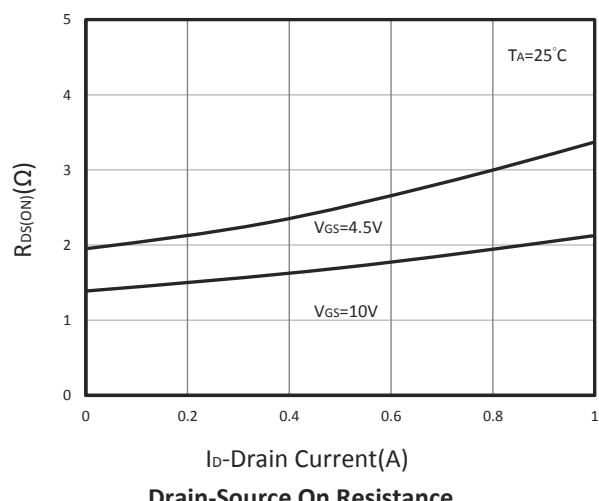
The products and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date. We assume no responsibility for any infringement of patents, patent rights, or other rights arising from the use of any information and circuitry in this datasheet.

■ TYPICAL CHARACTERISTICS



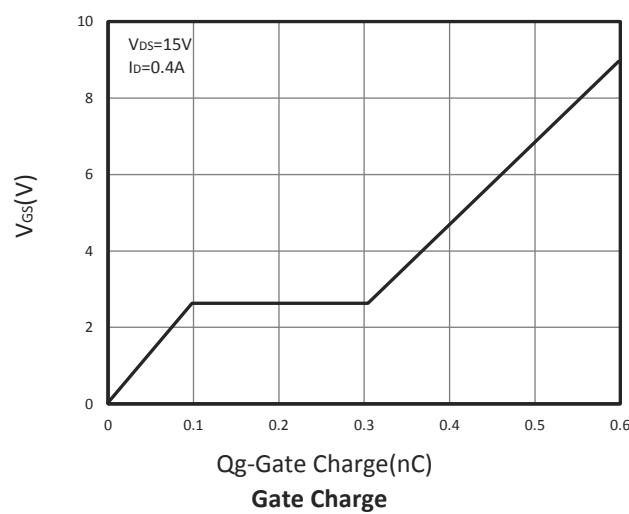
V_{DS}-Drain Source Voltage(V)

Output Characteristics



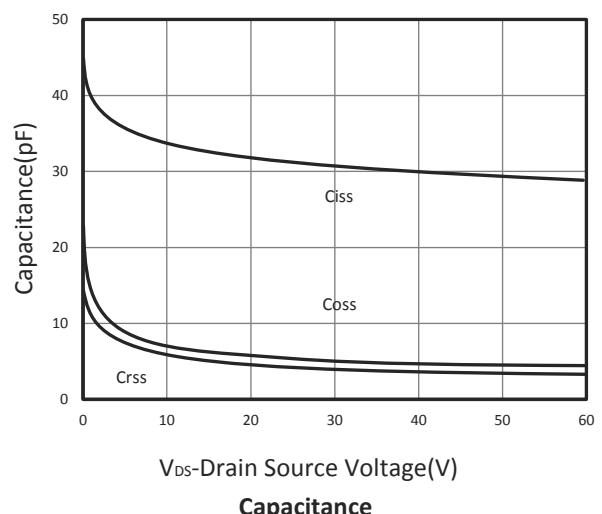
I_D -Drain Current(A)

Drain-Source On Resistance



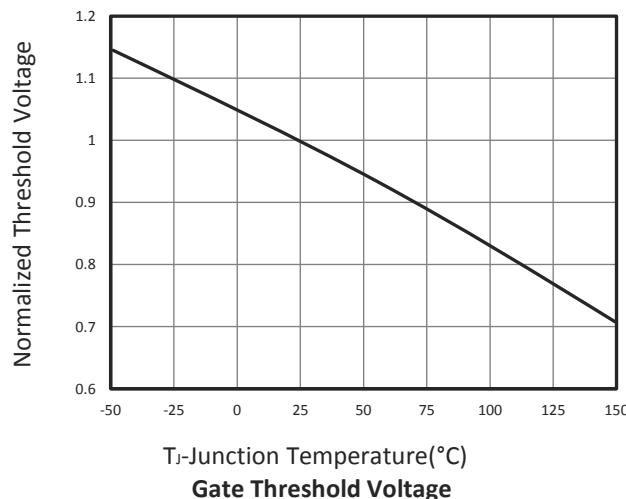
$V_{DS}=15V$

Gate Charge



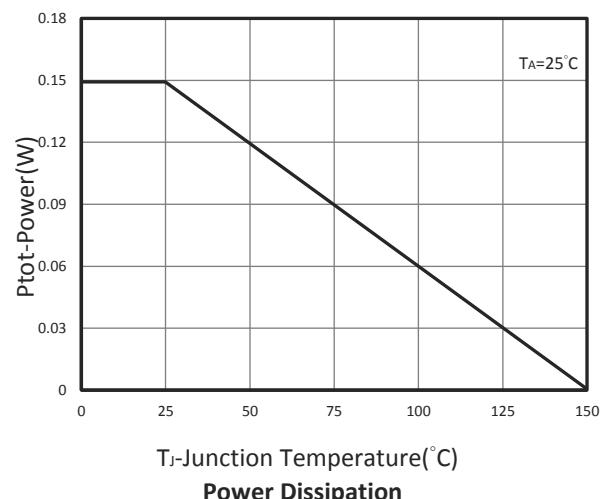
V_{DS} -Drain Source Voltage(V)

Capacitance



T_J -Junction Temperature(°C)

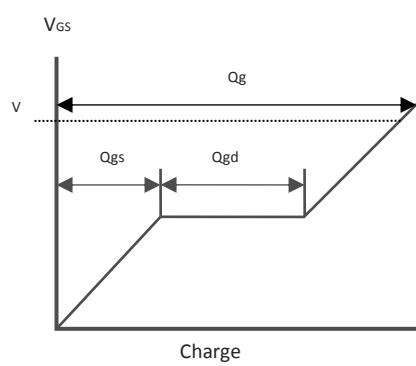
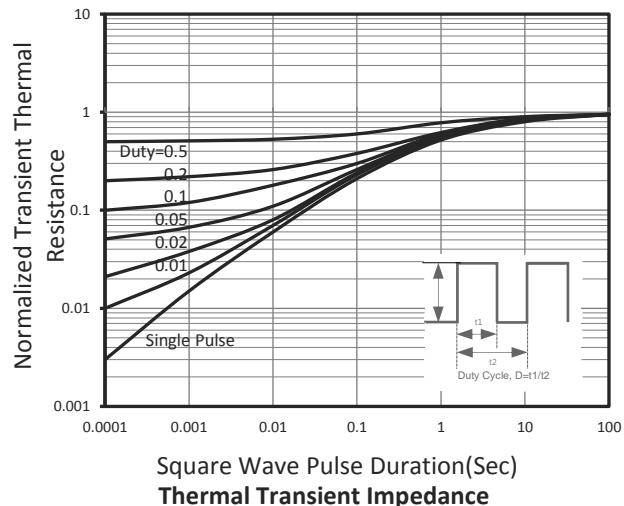
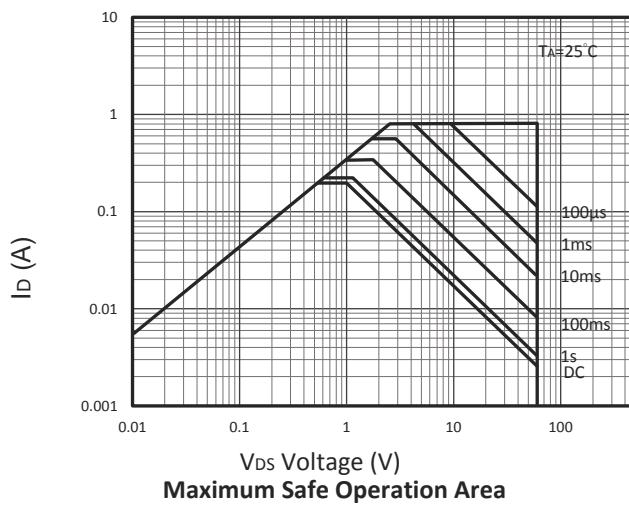
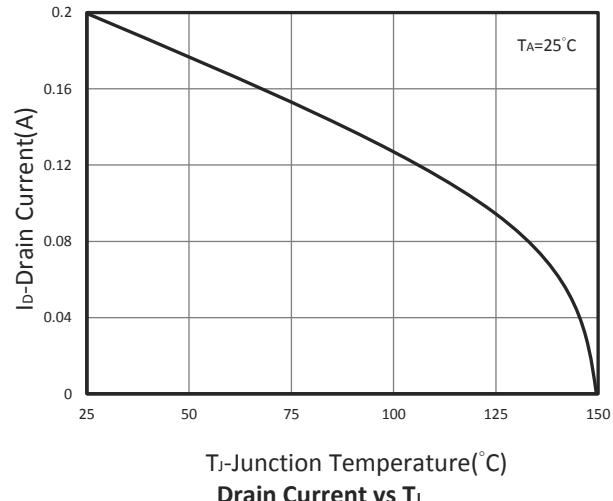
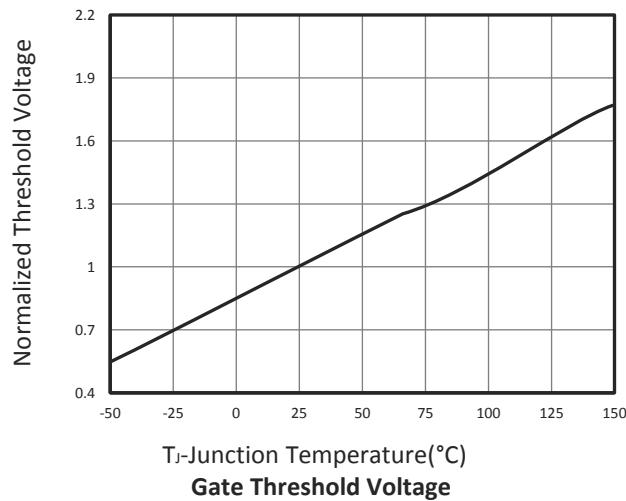
Gate Threshold Voltage



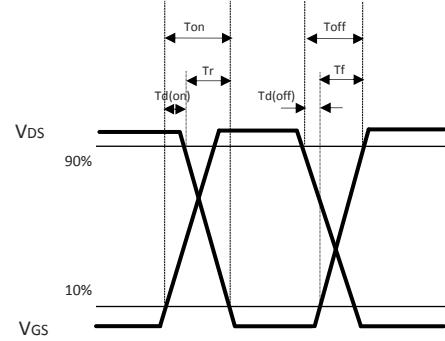
T_J -Junction Temperature(°C)

Power Dissipation

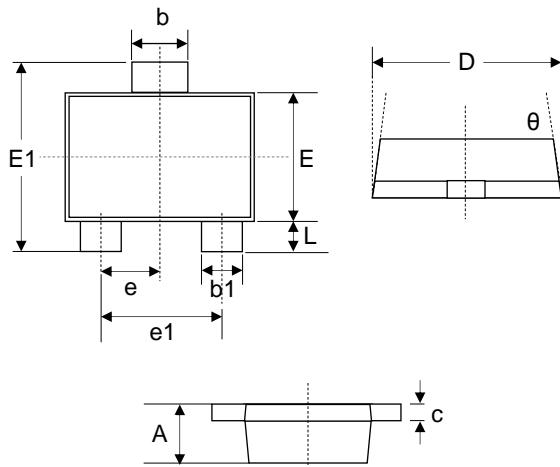
TYPICAL CHARACTERISTICS



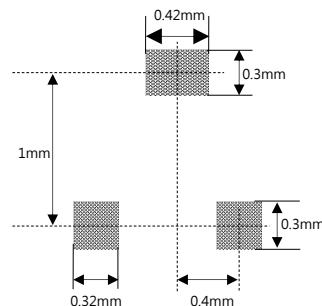
Gate Charge Waveform



Switching Time Waveform

SOT-723 PACKAGE DIMENSIONS


Recommended Land Pattern



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.430	0.500	0.017	0.020
b	0.270	0.370	0.011	0.015
b1	0.170	0.270	0.007	0.011
c	0.001	0.015	0.003	0.006
D	1.150	1.250	0.045	0.049
E	0.750	0.850	0.030	0.033
E1	1.150	1.250	0.045	0.049
e	0.400 BSC.		0.016 BSC.	
e1	0.800 BSC.		0.032 BSC.	
L	0.200 BSC.		0.008 BSC	
θ	7°		7°	